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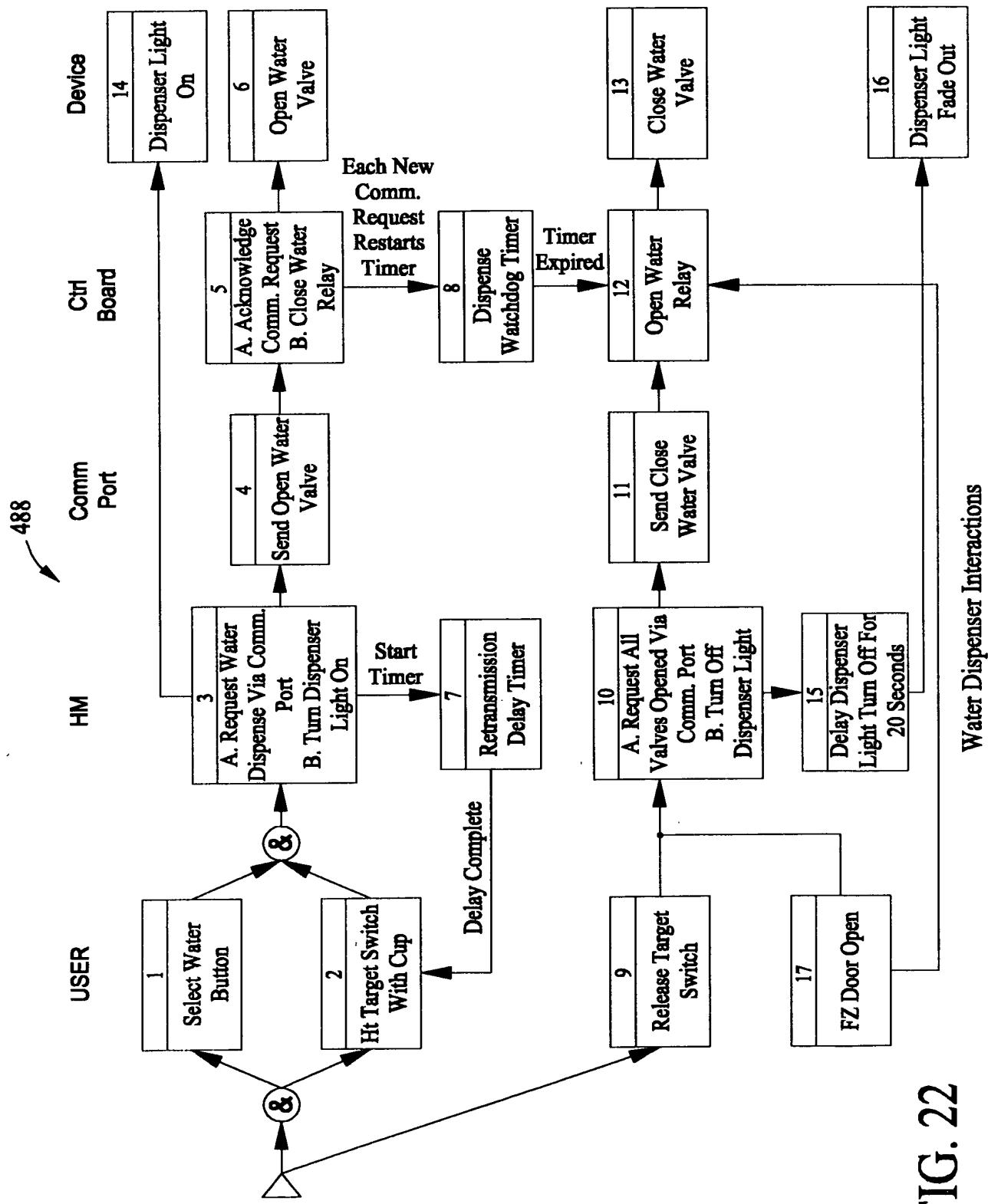
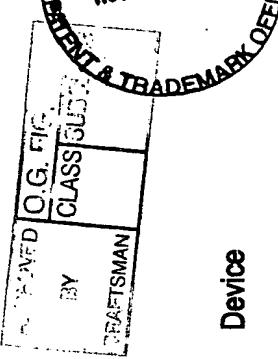
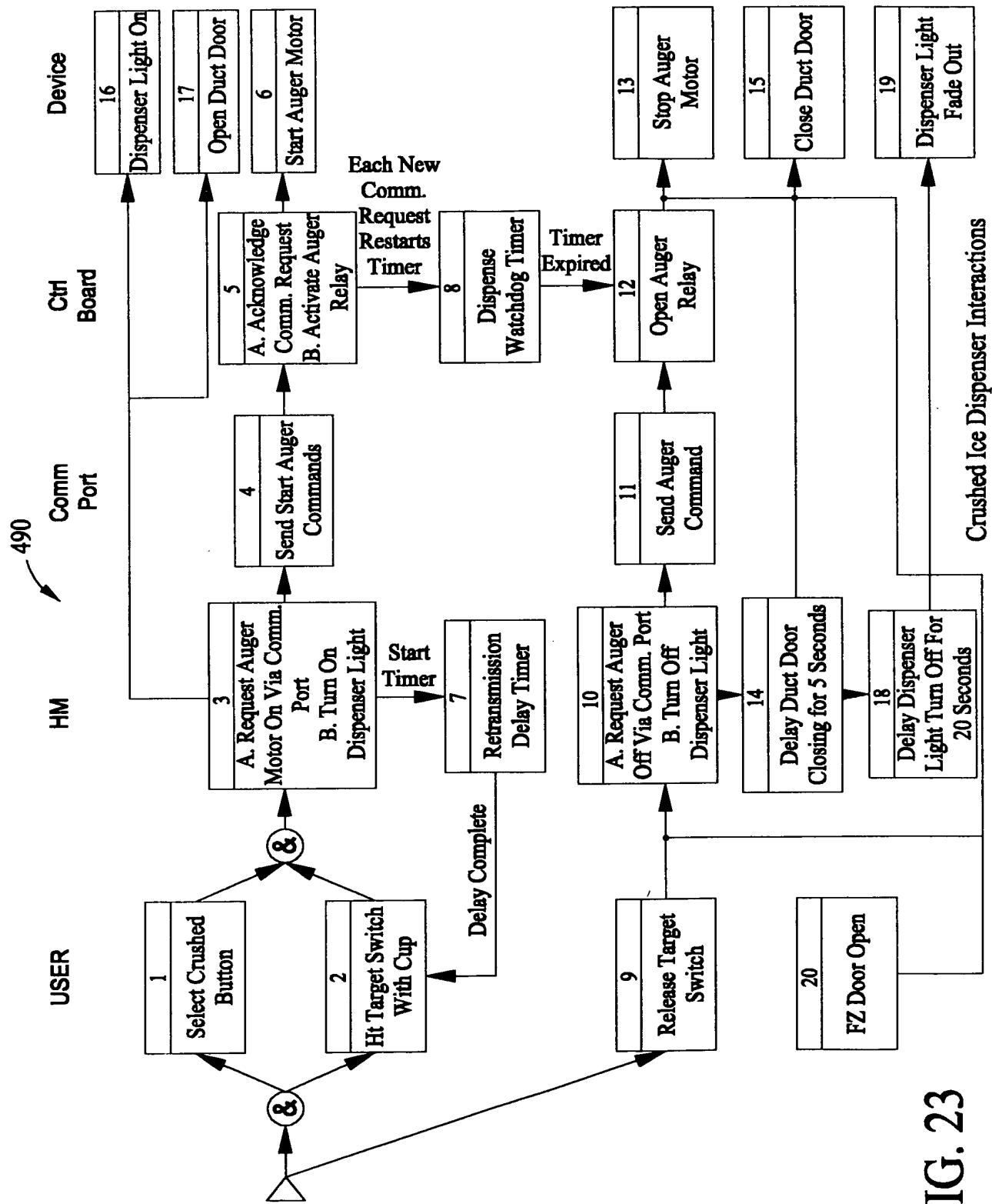


FIG. 22

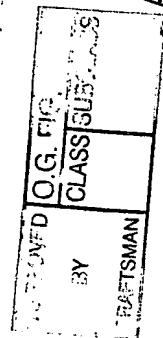


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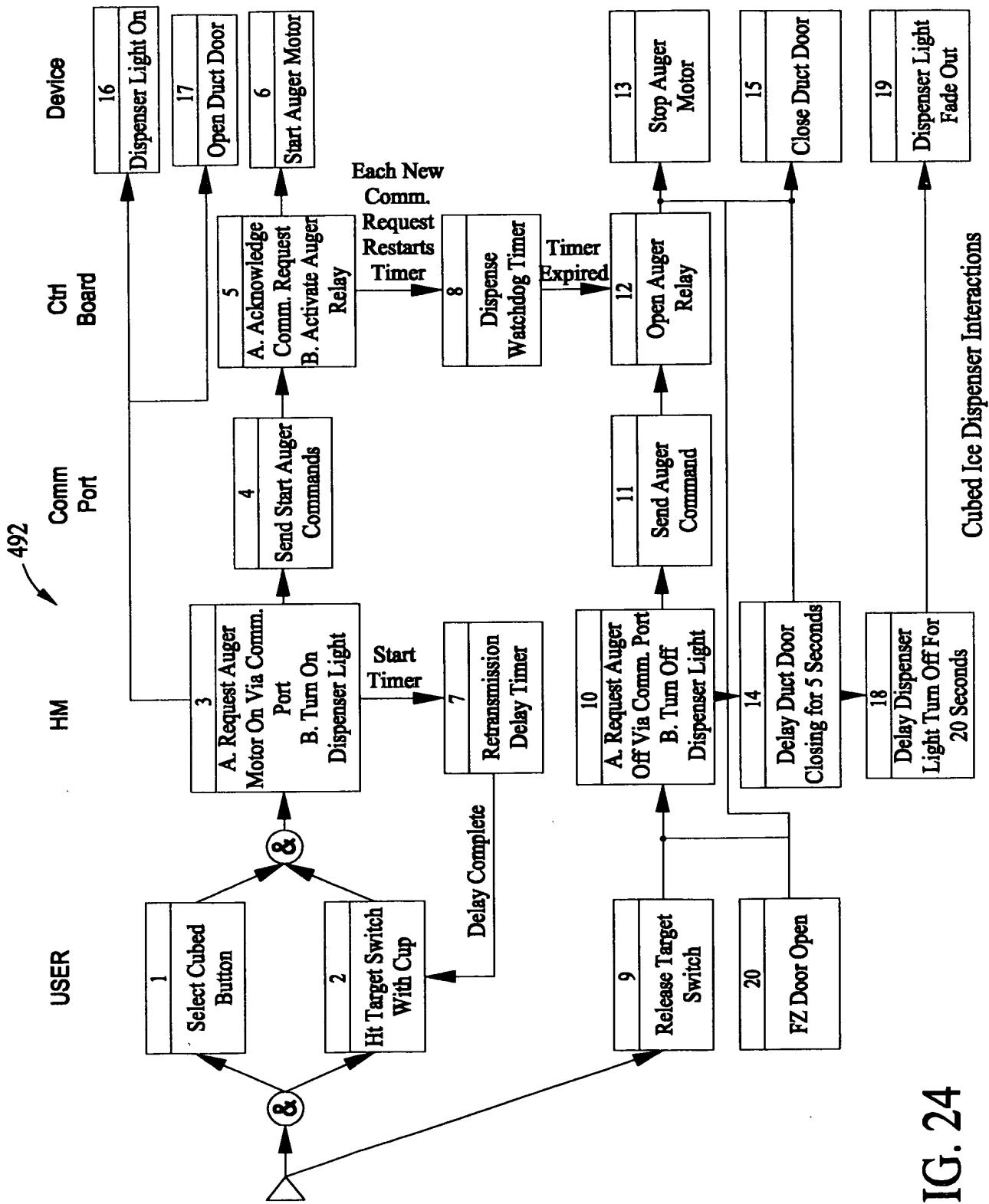


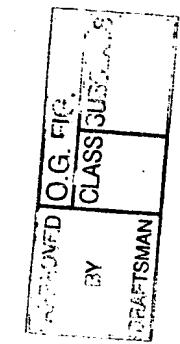
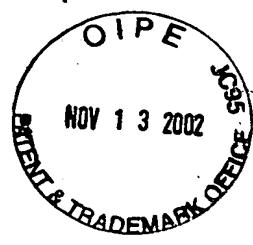
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 Atty. Dkt. No.: 9D-HR-19396
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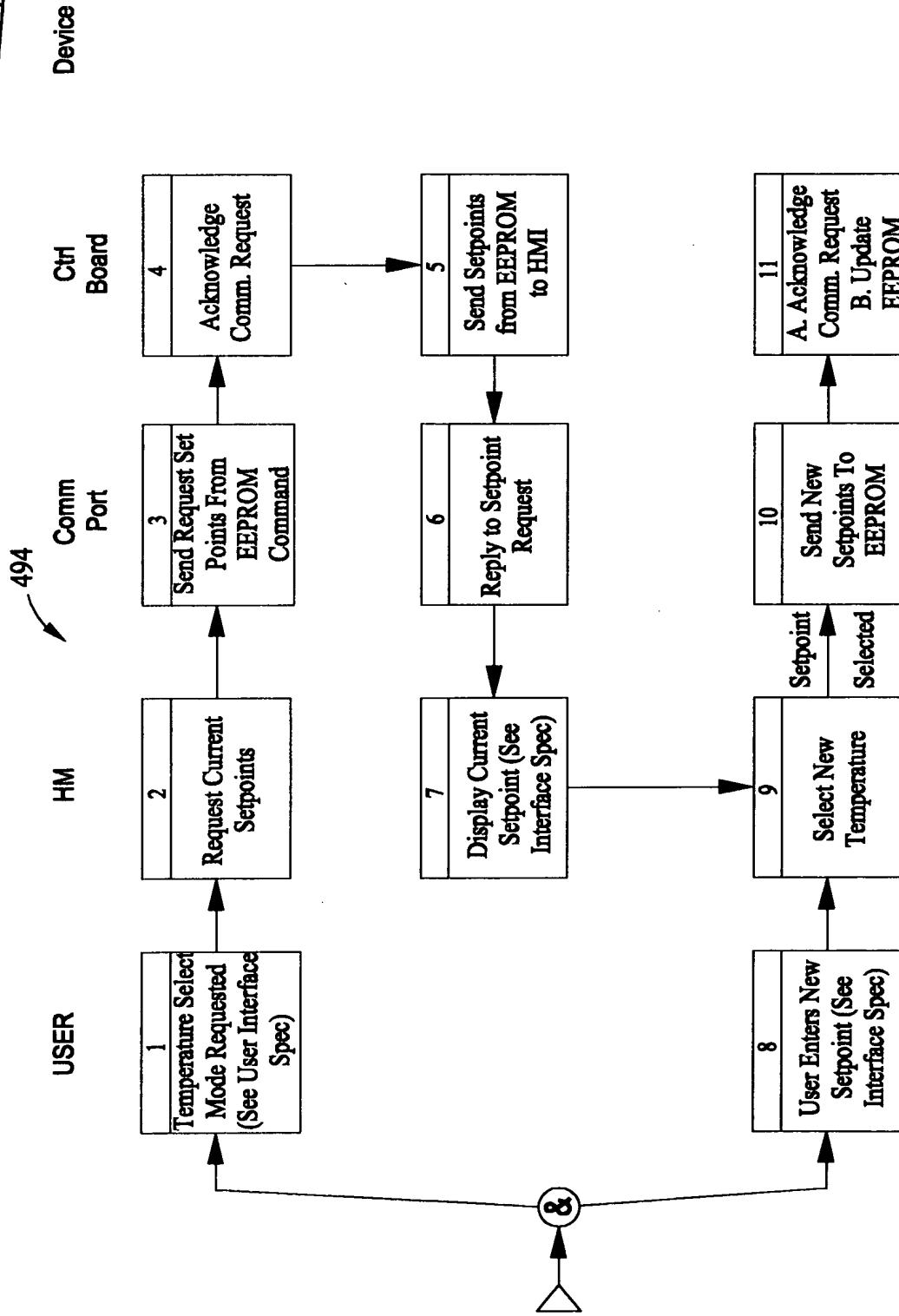


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NOTE: Setpoint Selected implies that the final selection has been made and that the selection has timed out.

FIG. 25

Temperature Setting Interaction Diagrams

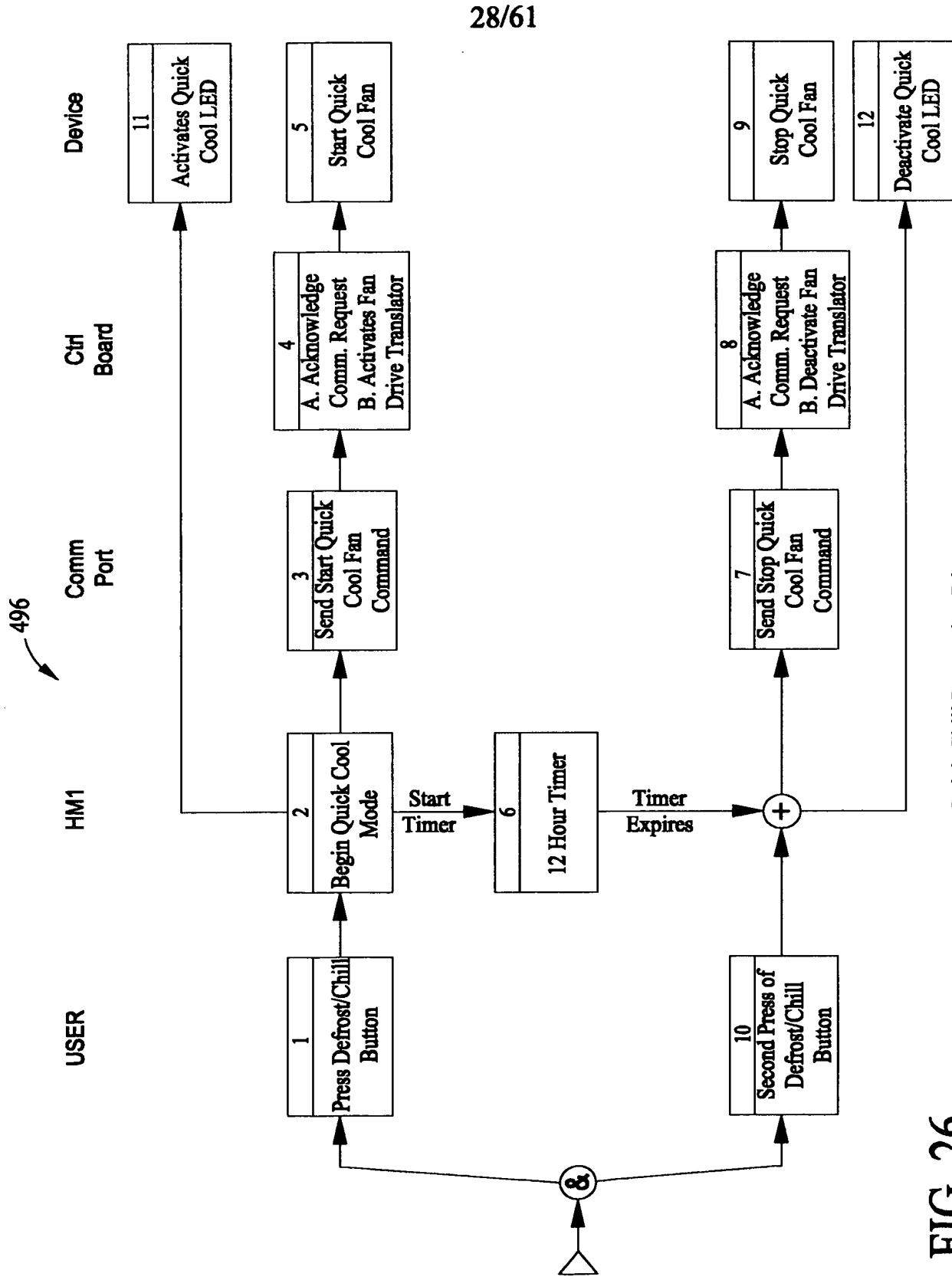
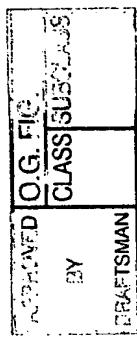


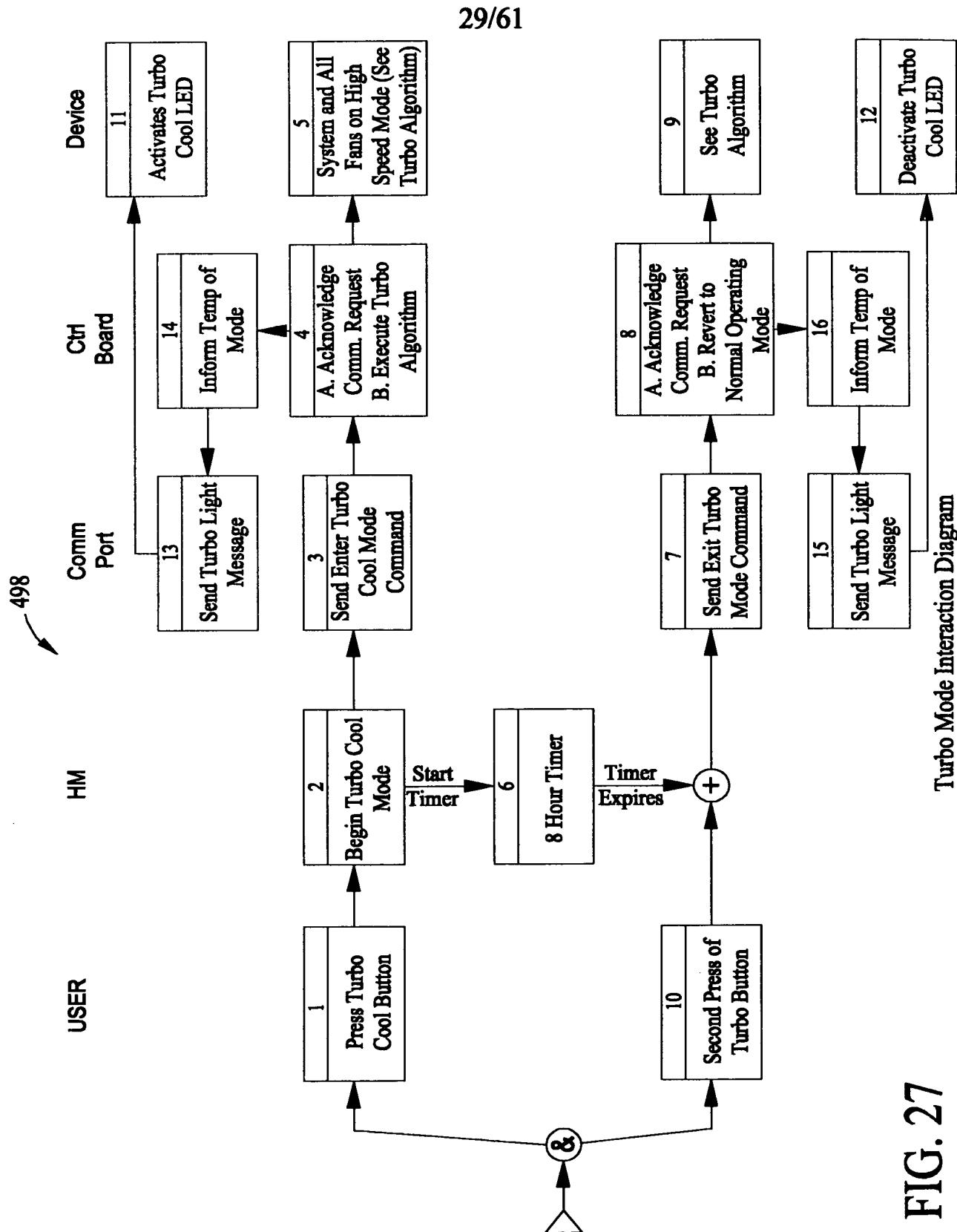
FIG. 26

Quick Chill Interaction Diagram

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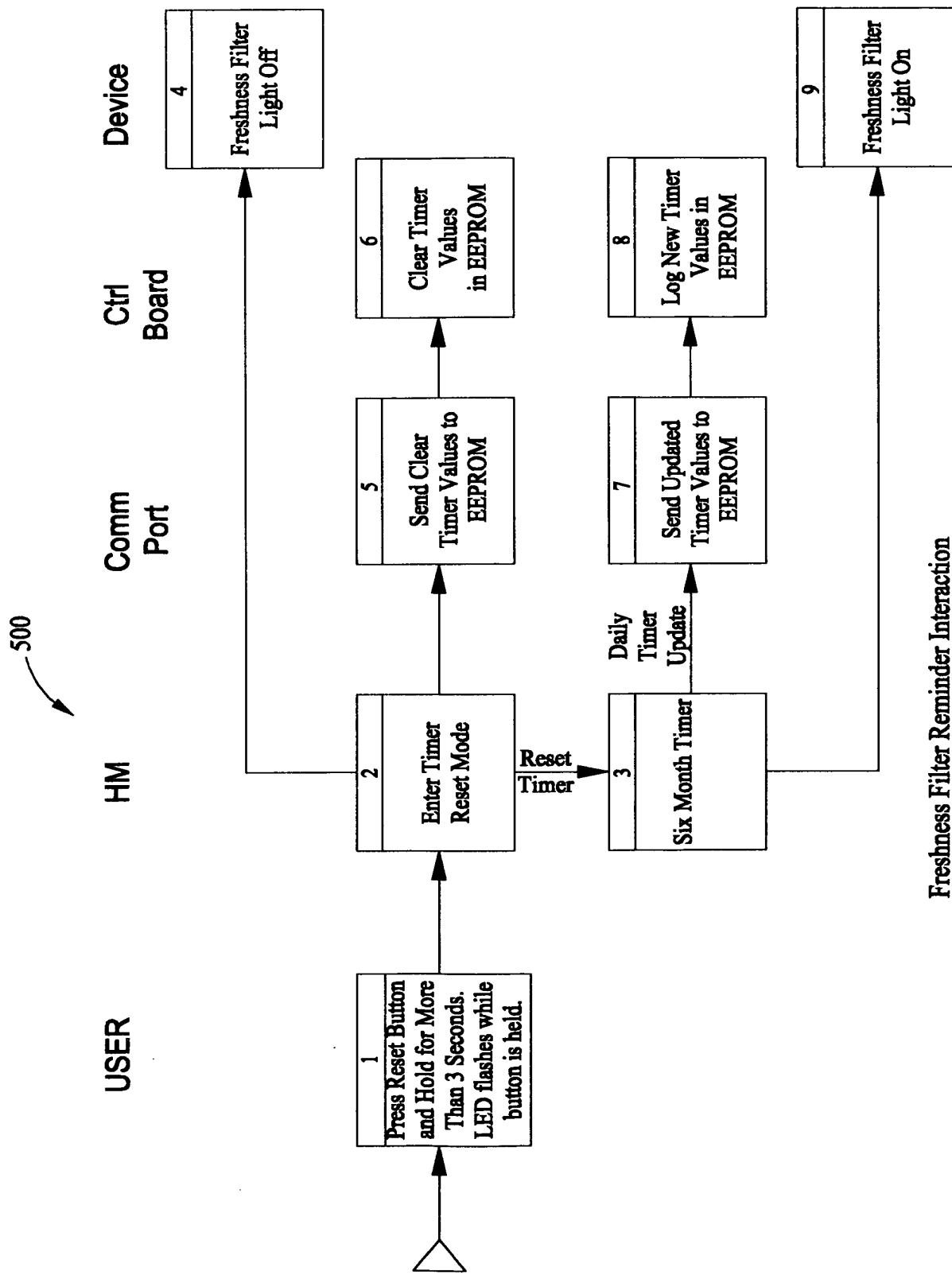
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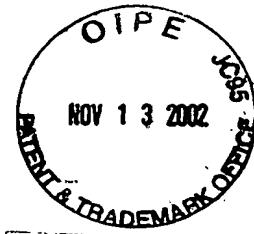
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Freshness Filter Reminder Interaction

FIG. 28



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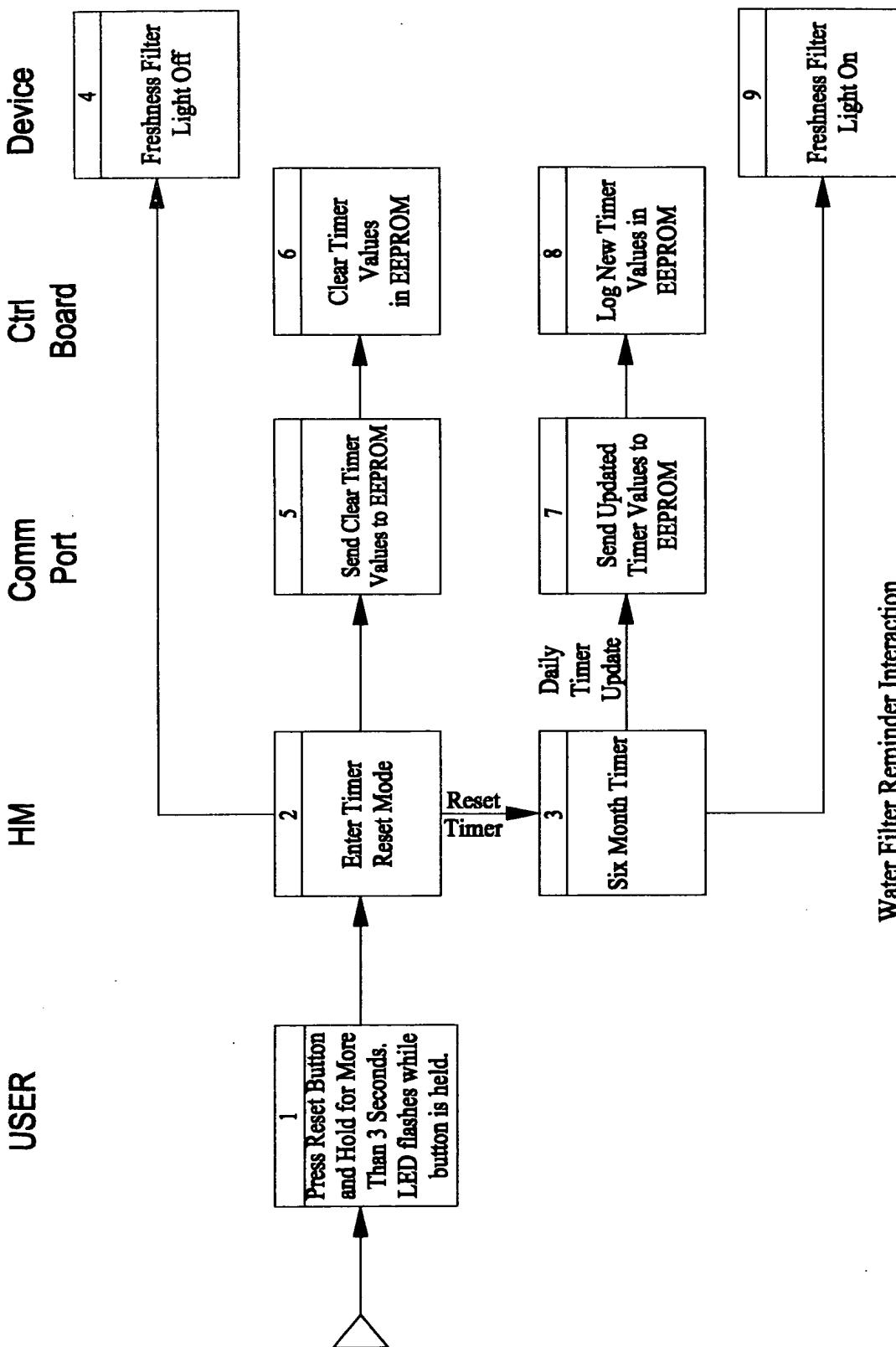
USER

三

Comm
Port

Ctrl Board

Device

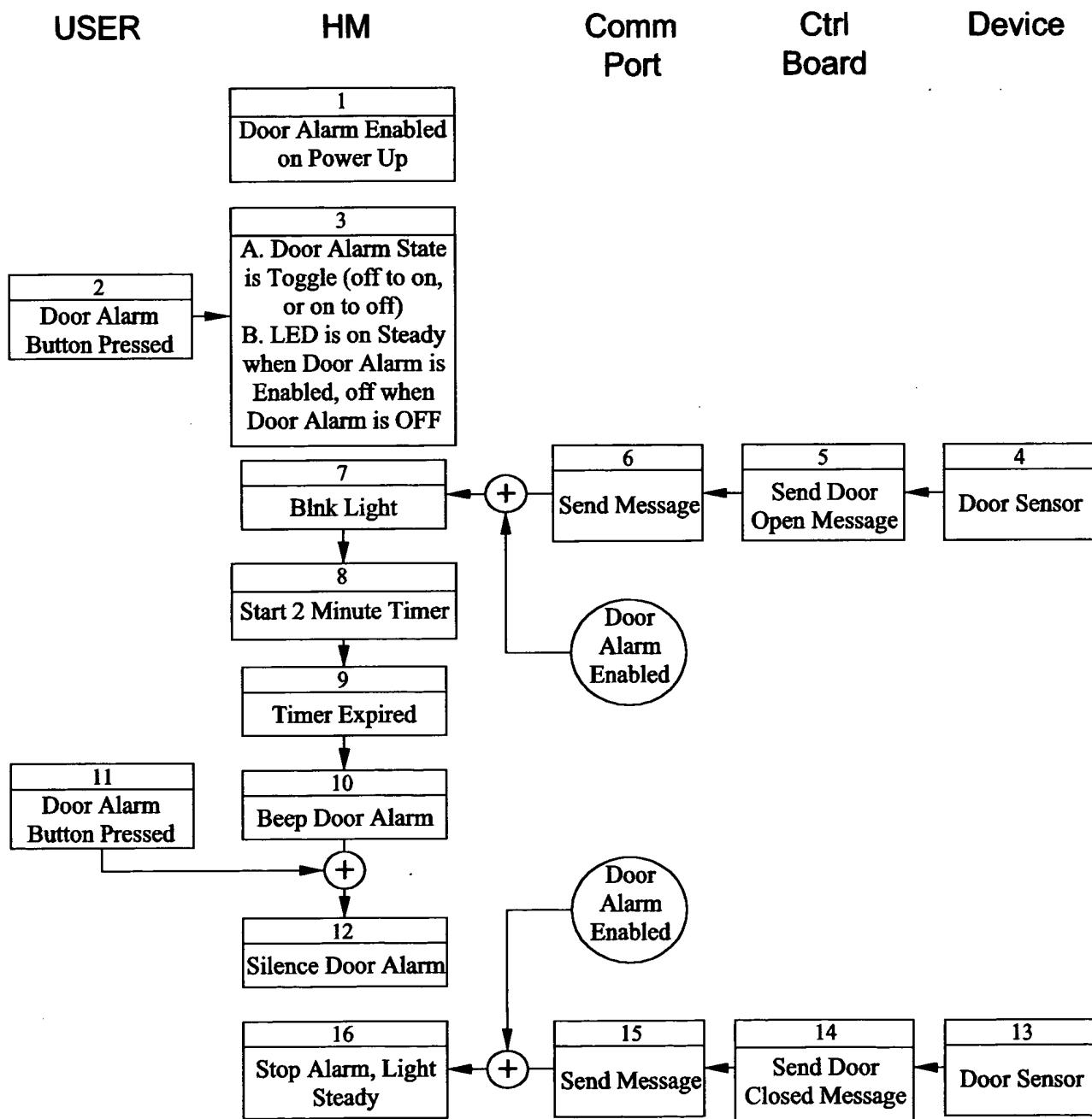


Water Filter Reminder Interaction

FIG. 29

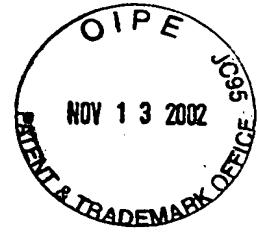
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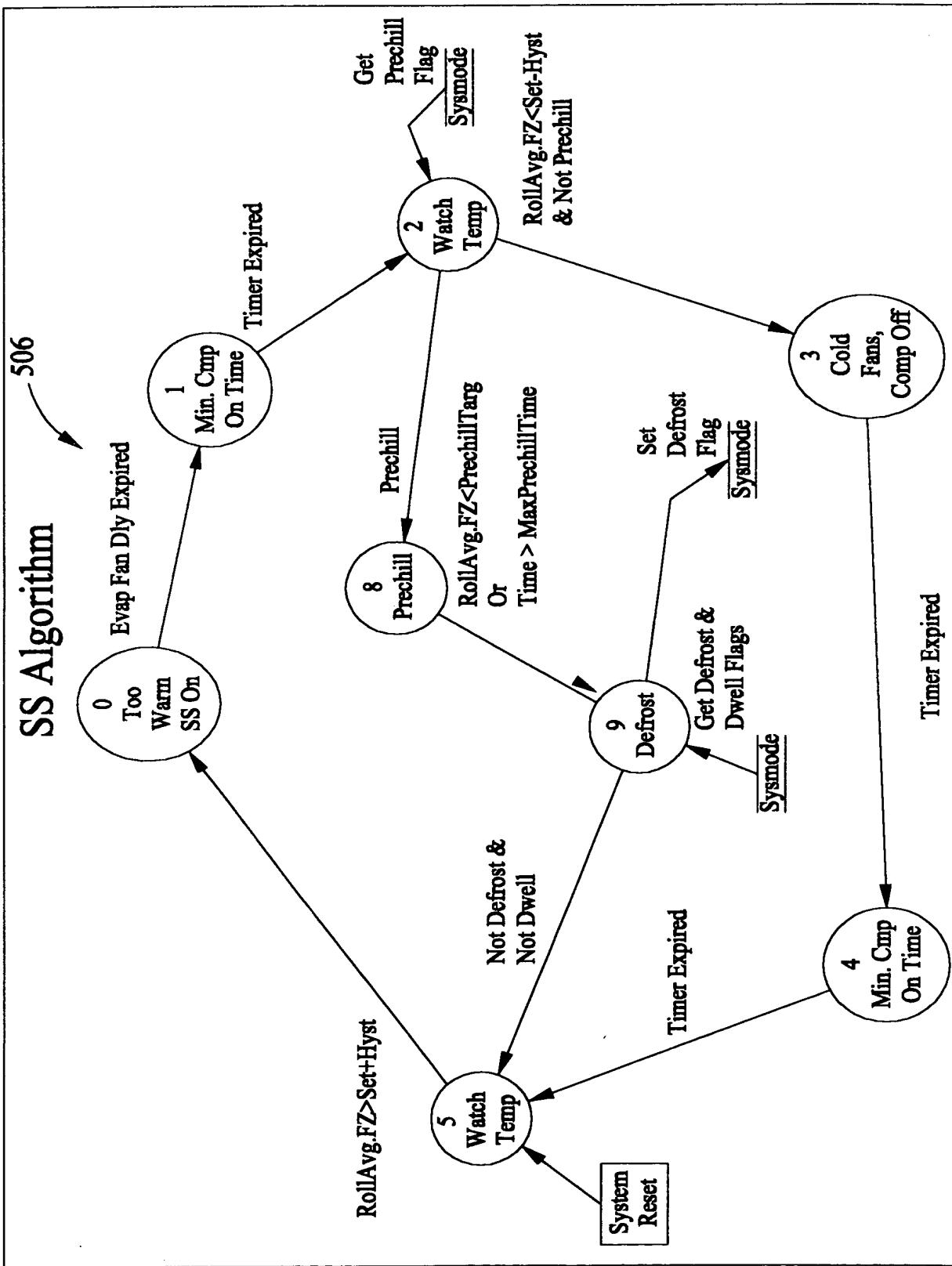
Door Open Interaction Diagram

FIG. 30

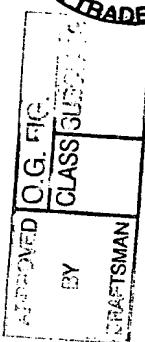
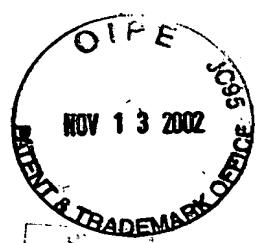


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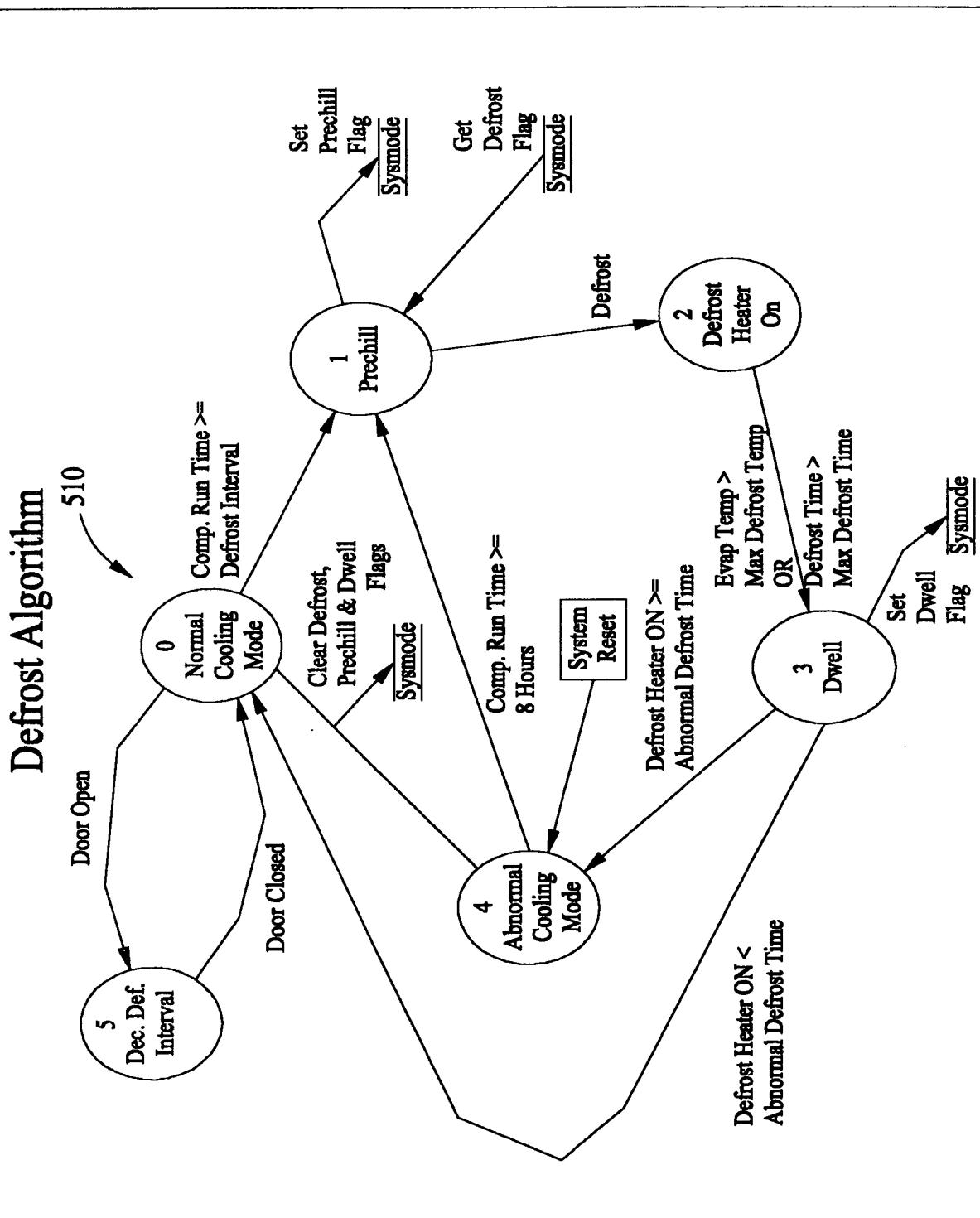
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Sealed System Operational Algorithm

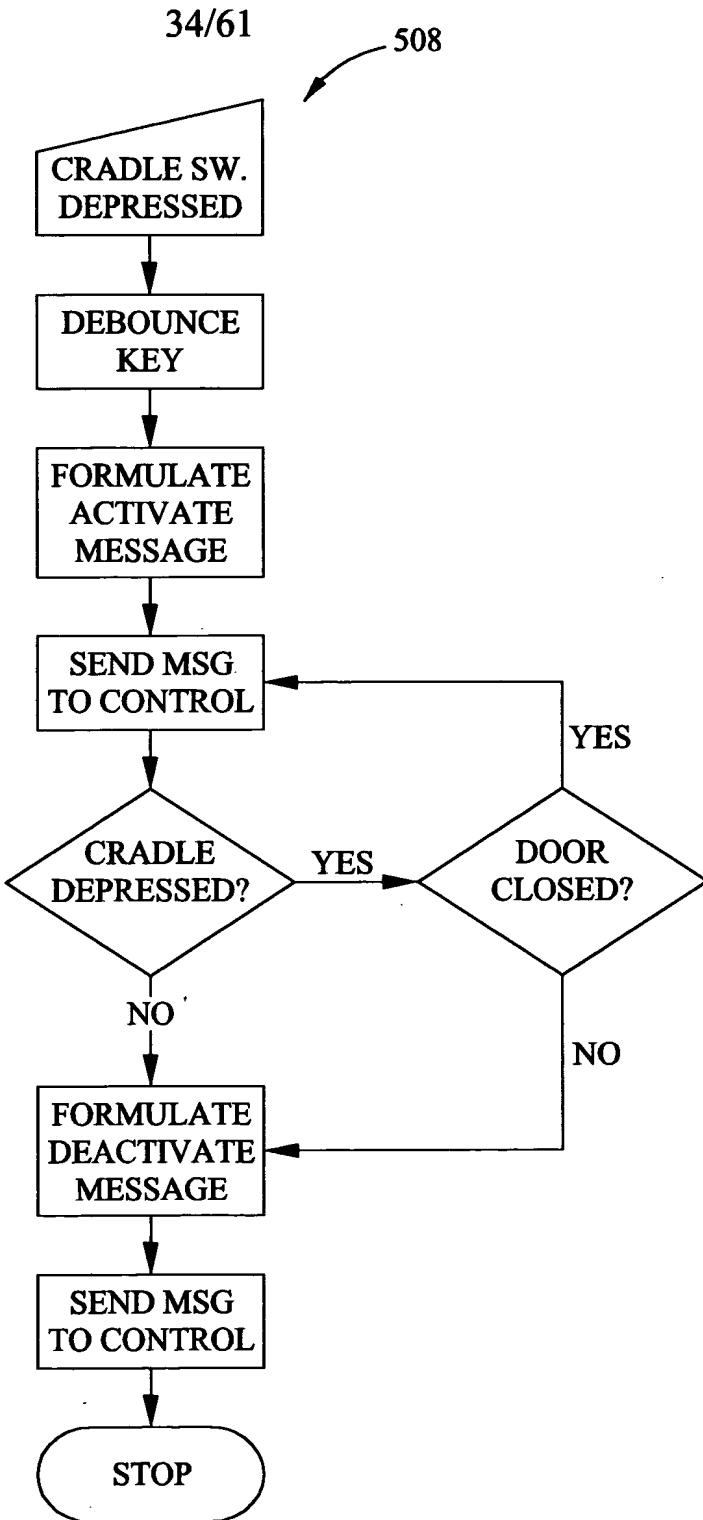
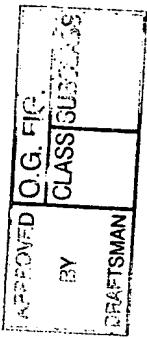


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Defrost Control State Diagram

FIG. 33

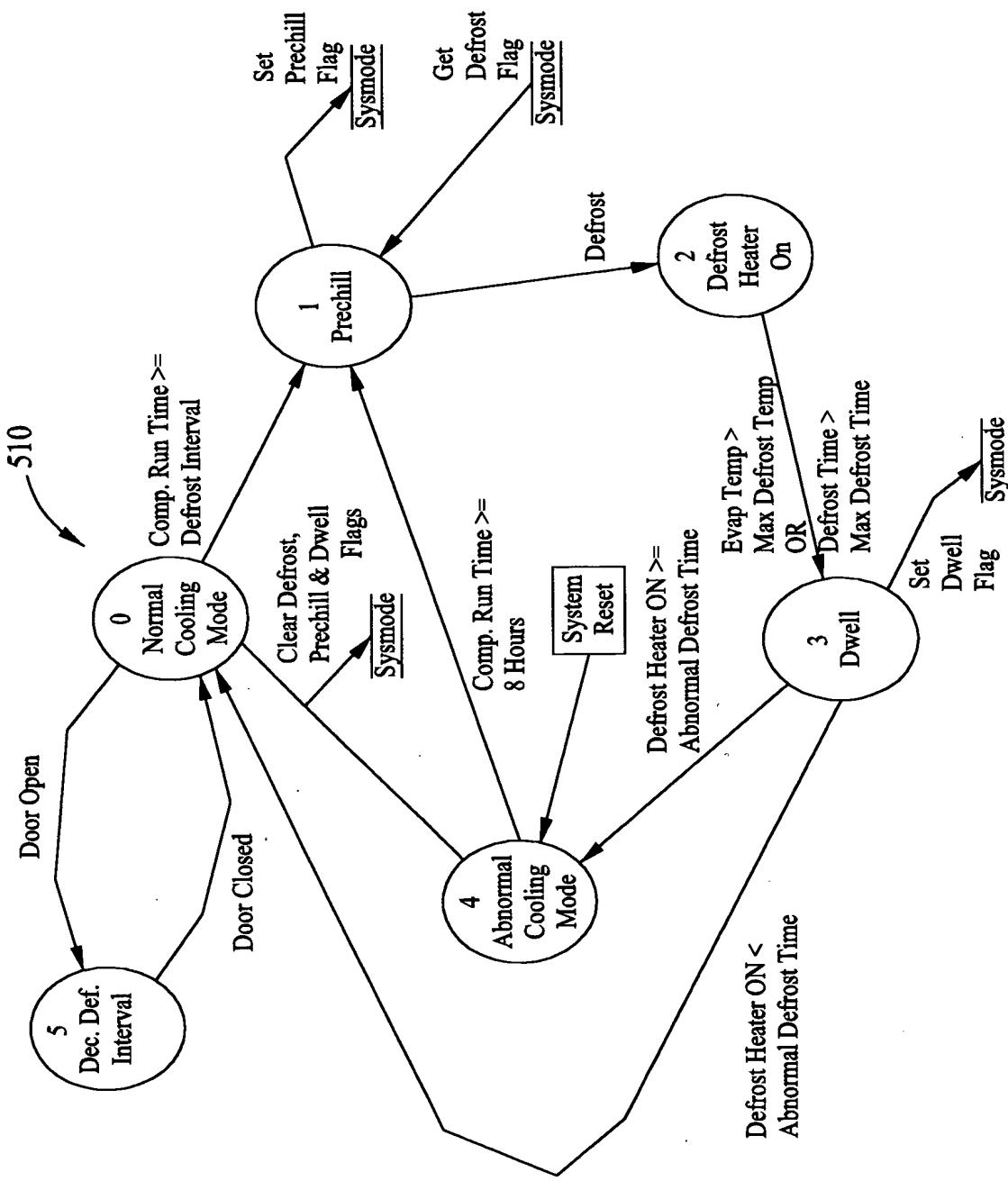


Dispenser Control Algorithm

FIG. 32

| | |
|----------|-------------|
| APPROVED | O.G. FIG. |
| | CLASS SUBS. |
| BY | DRAFTSMAN |

Defrost Algorithm



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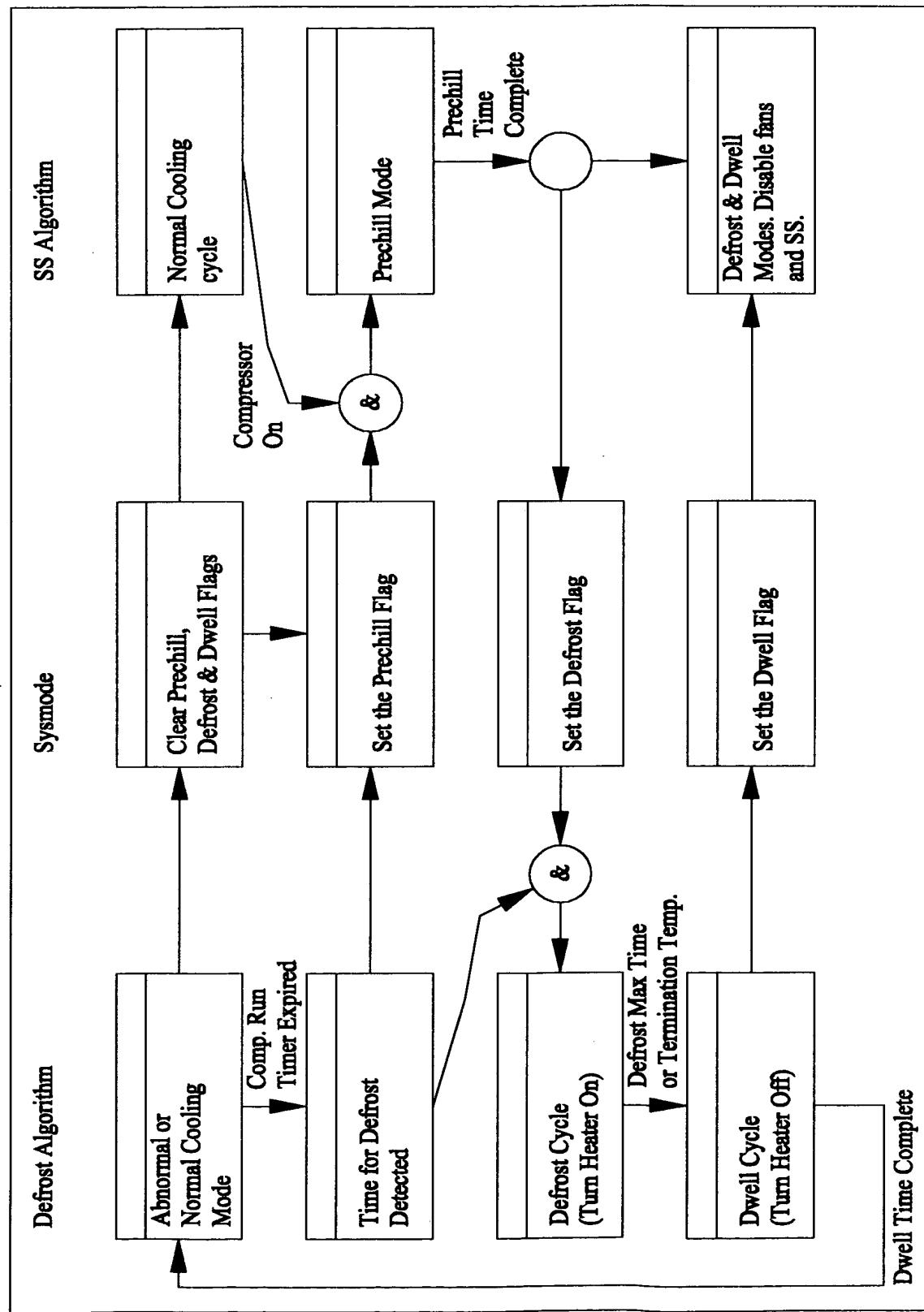
Defrost Control State Diagram

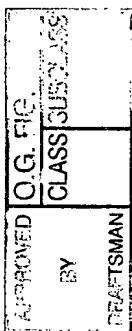
FIG. 33



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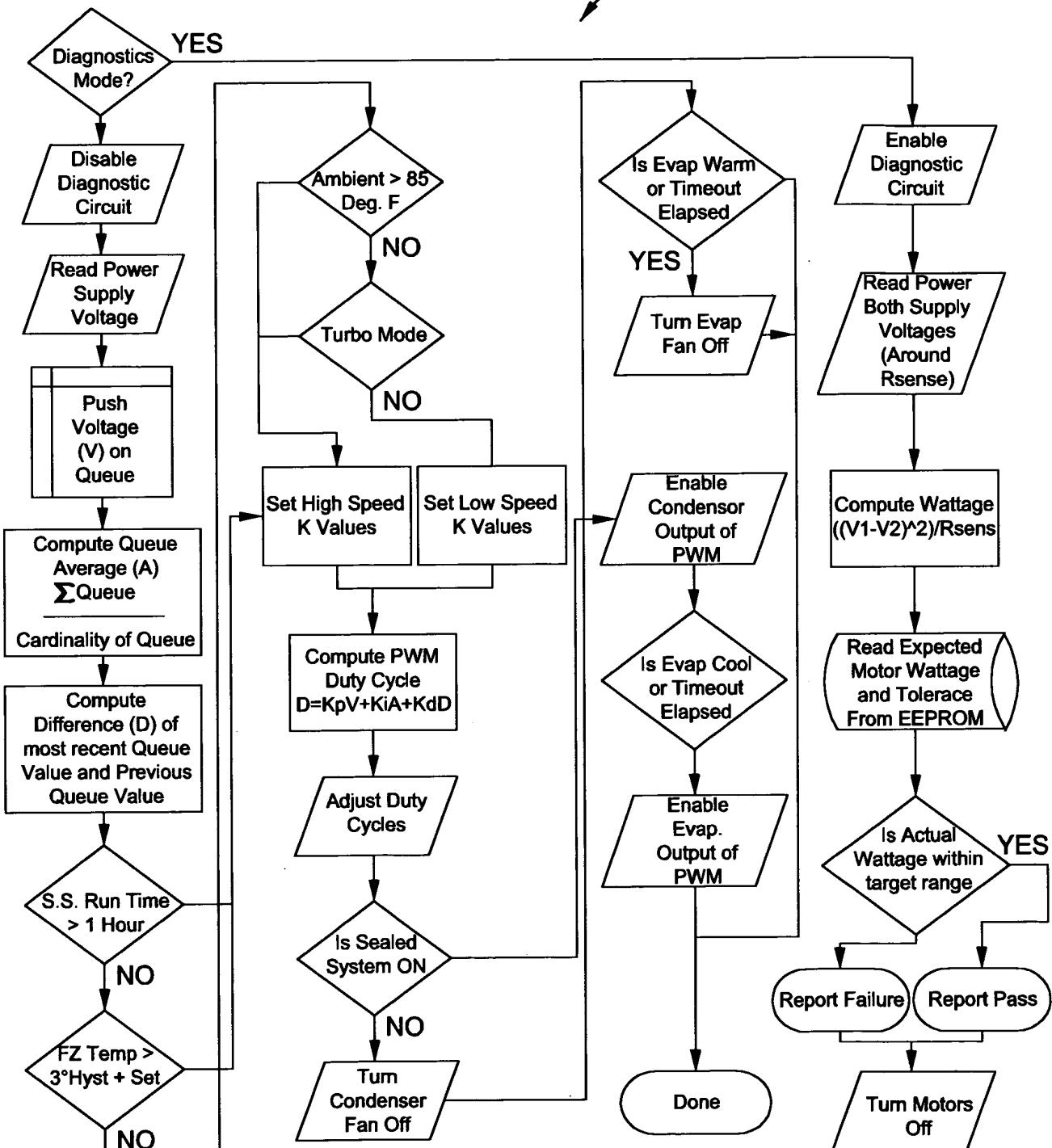




Evap. & Cond.Fan Control:

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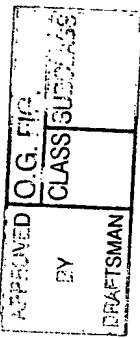
514



Fan Speed Control

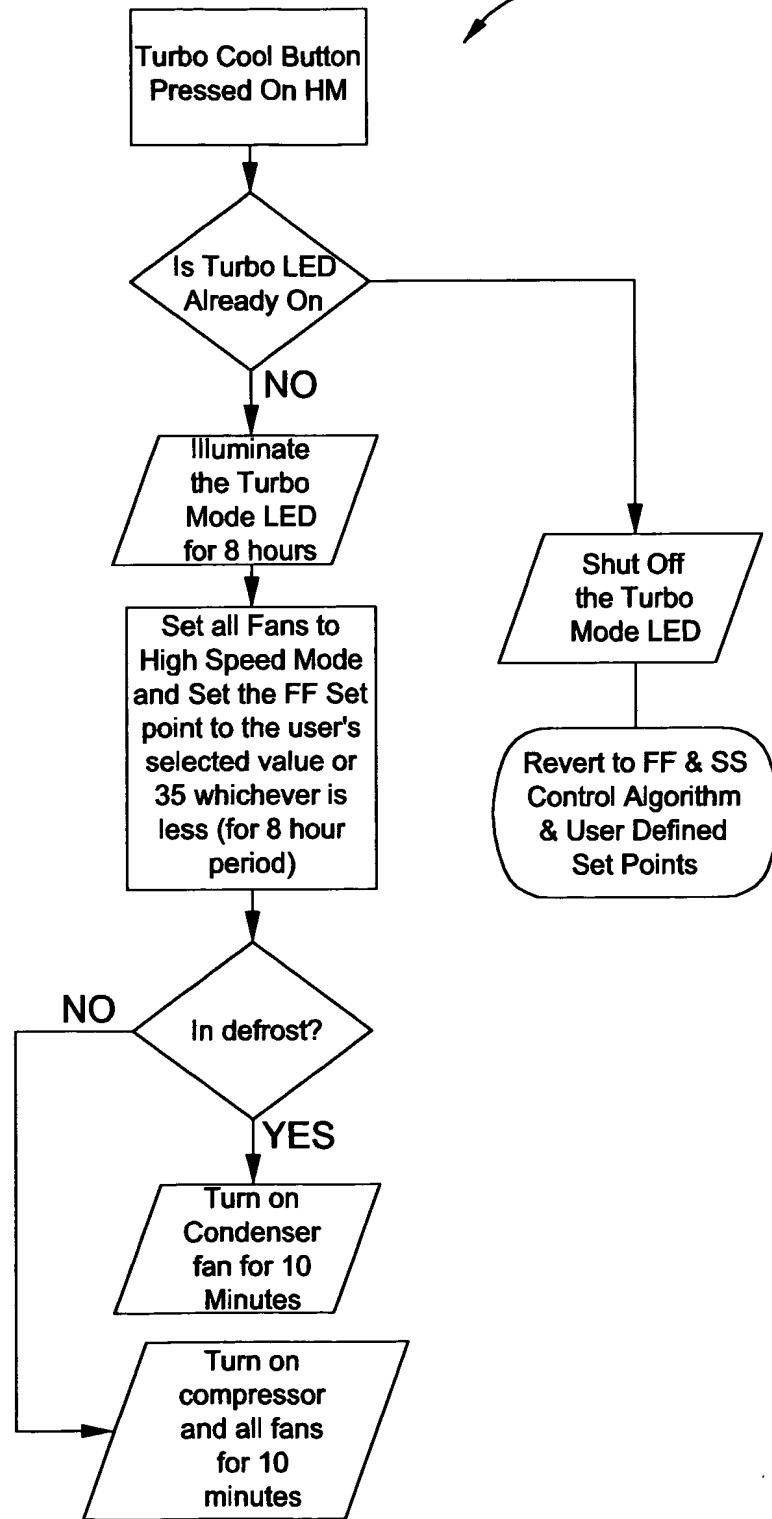
- Notes:
1. The FF & Evaporator fans will shut off for the first five minutes that the door is open
 2. Only one fan at a time can be on at a time during diagnostics.
 3. Once the fan has been switched to high speed, it remains in that state until the operational cycle is complete.

FIG. 35



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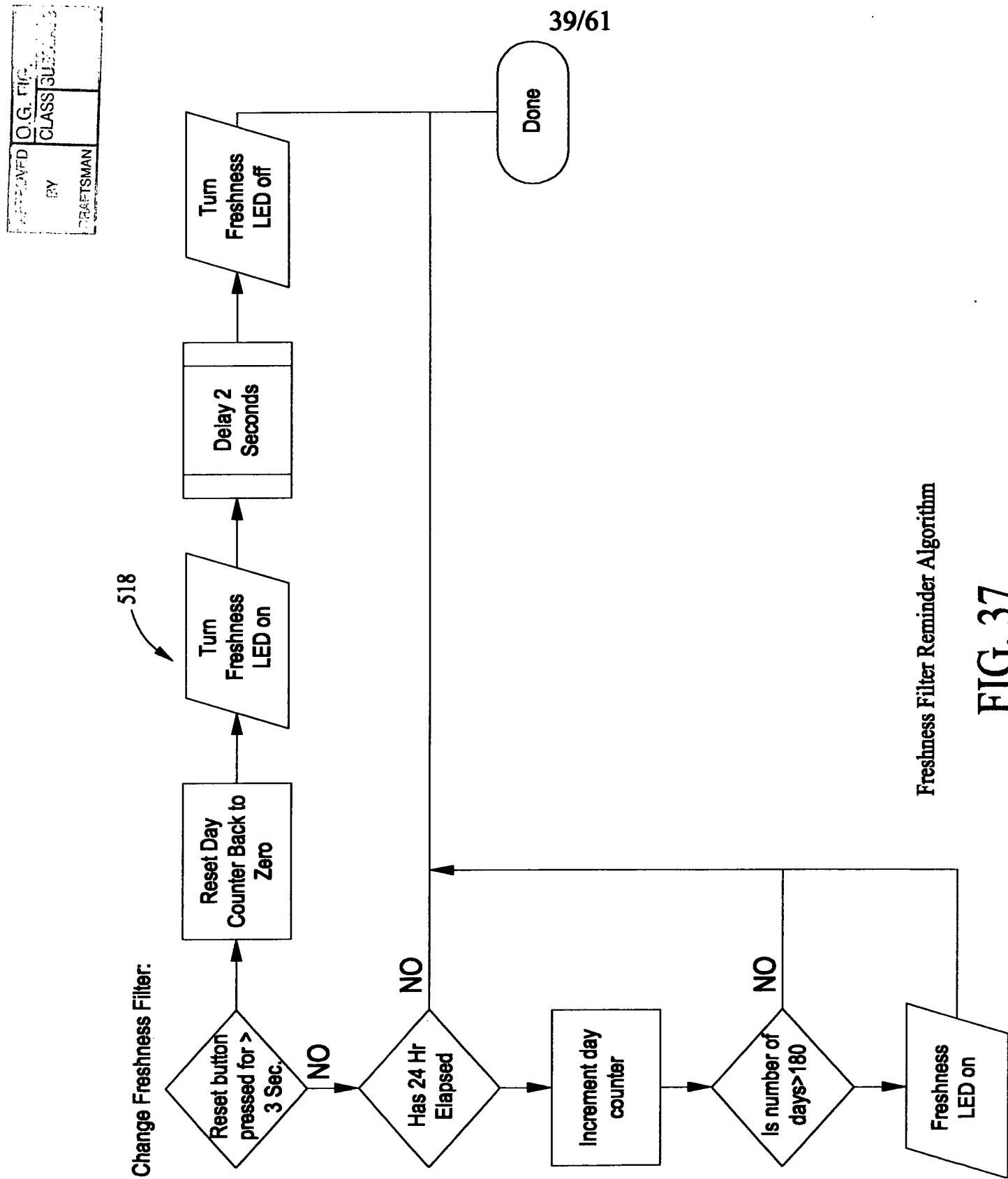


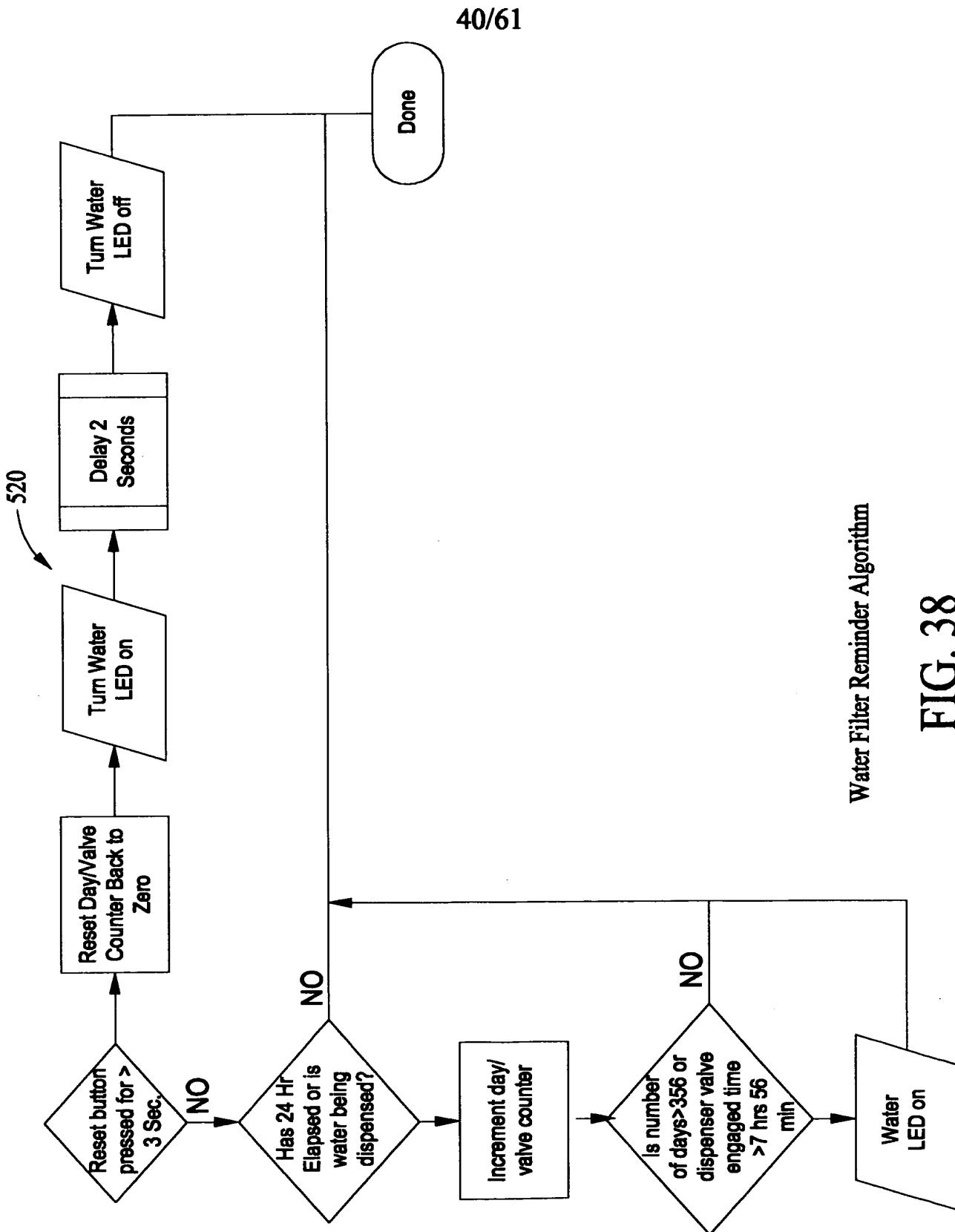
Turbo Cycle Algorithm

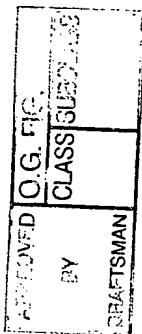
FIG. 36



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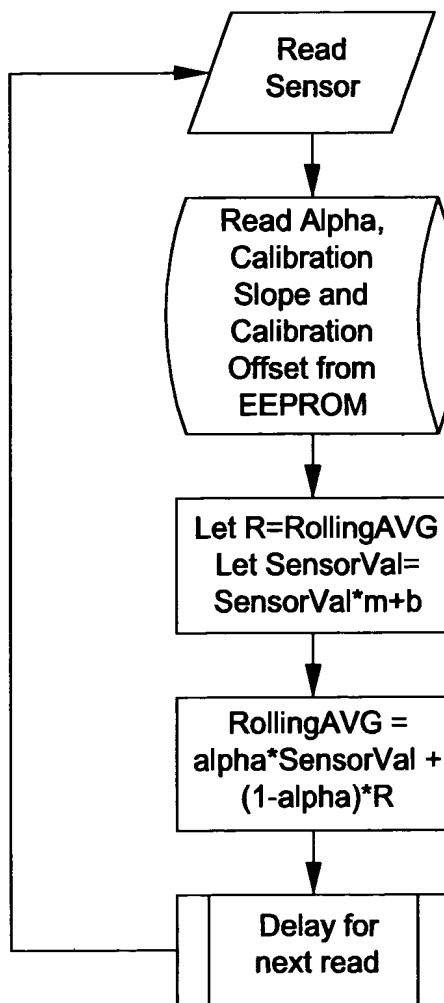






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SENSOR READ AND ROLLING AVERAGE ALGO:



NOTE: **Sensor Reading Algorithm**

Fresh food average uses this algorithm twice to create a 2nd pole filter.

FIG. 39

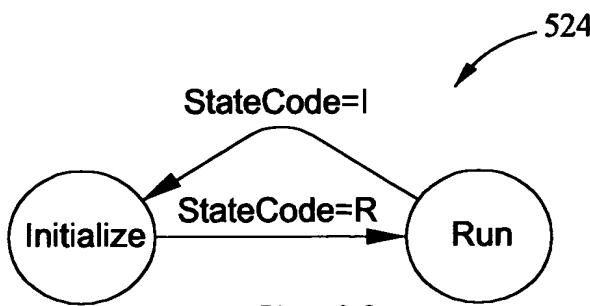


FIG. 40



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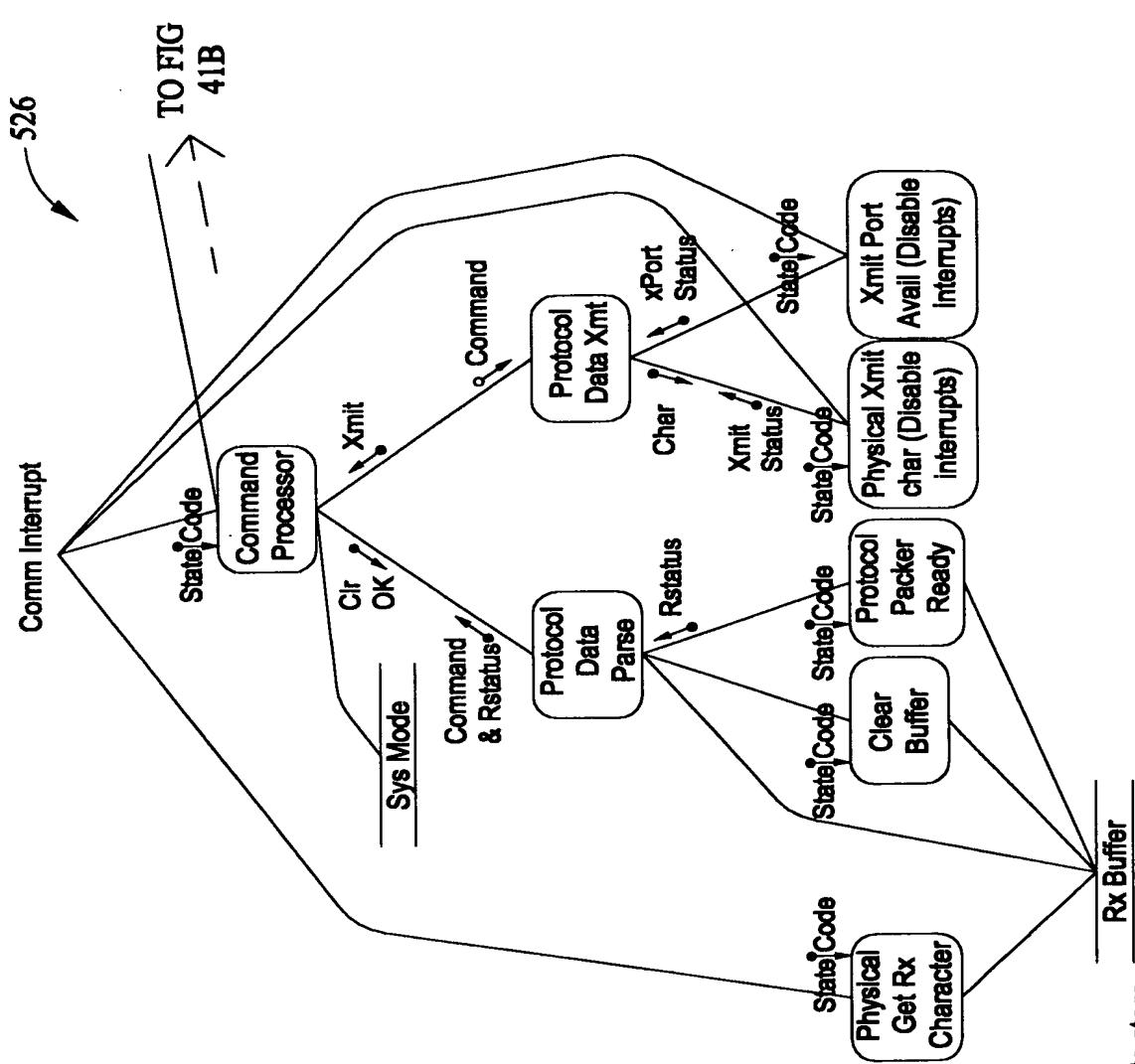


FIG. 41A

Note: Any Routine is allowed to read a data store
 The command processor can read or write
 any data store.

Dashed line indicates a path for initialization only



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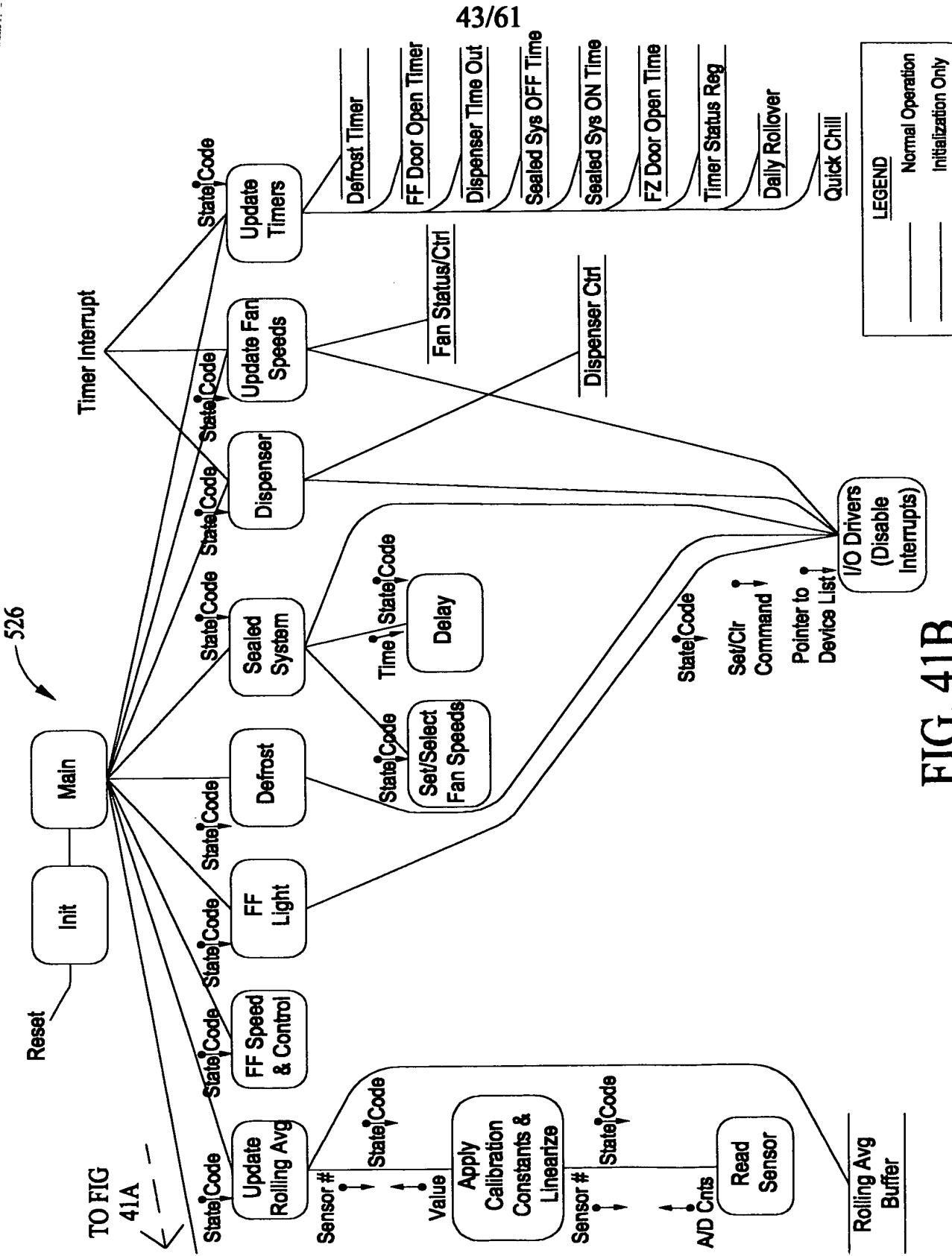
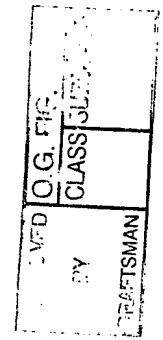
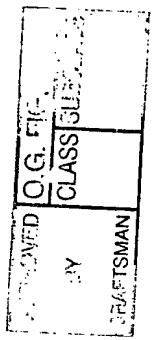
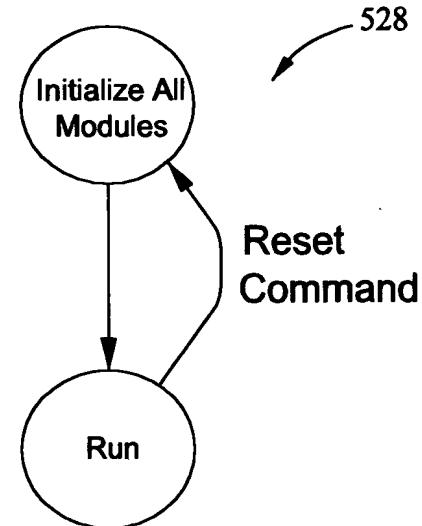


FIG. 41B



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State Diagram For Main Control

FIG. 42

HMI MAIN STATE MACHINE

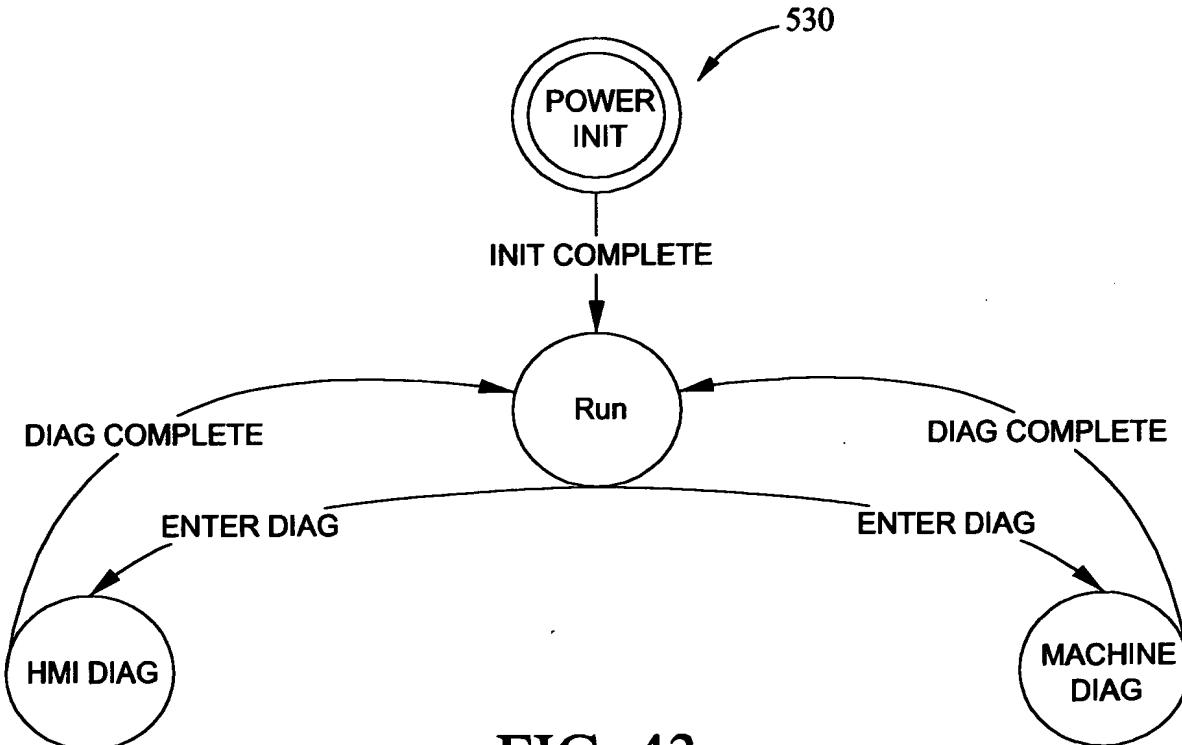
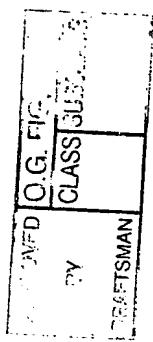
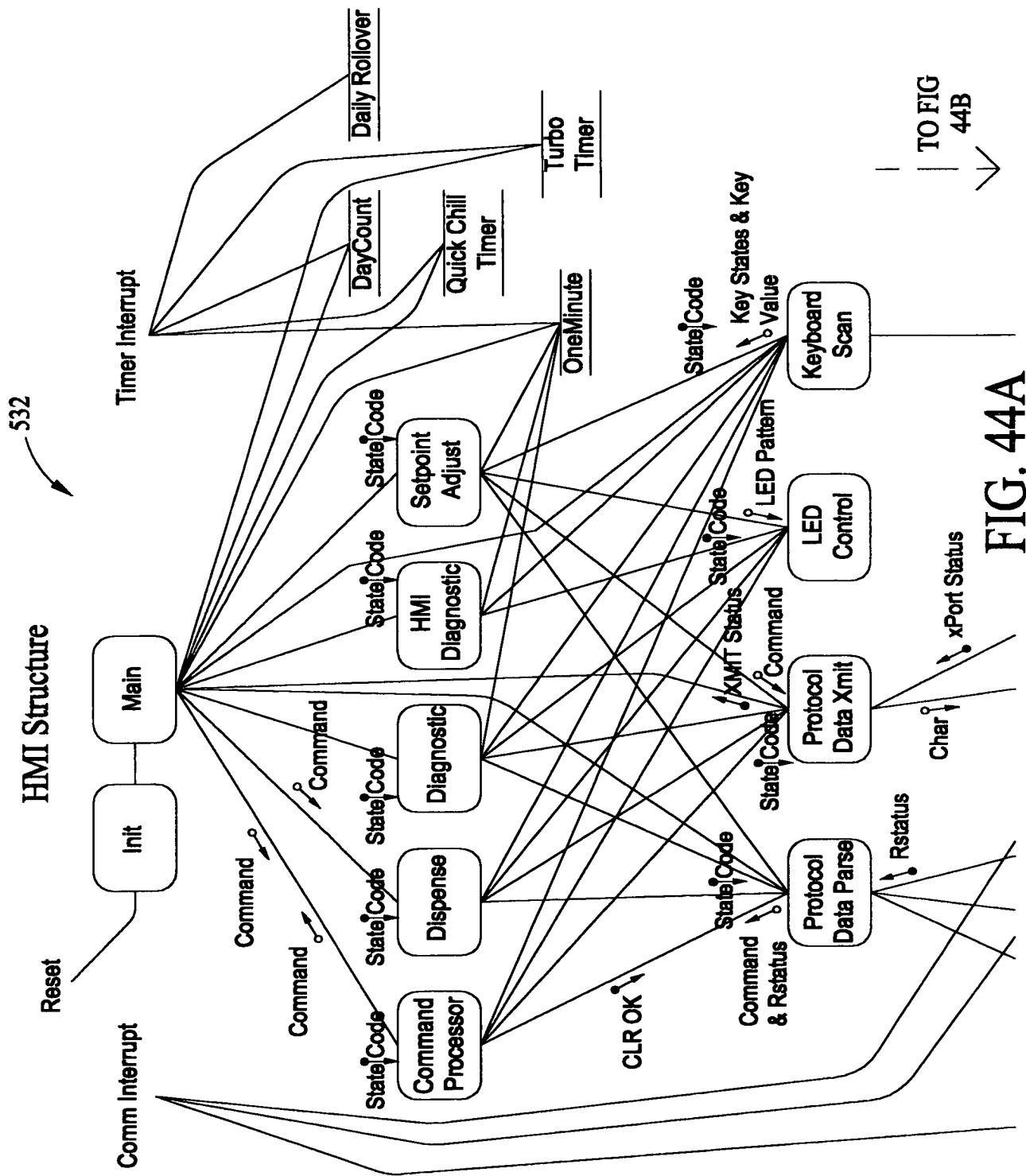


FIG. 43



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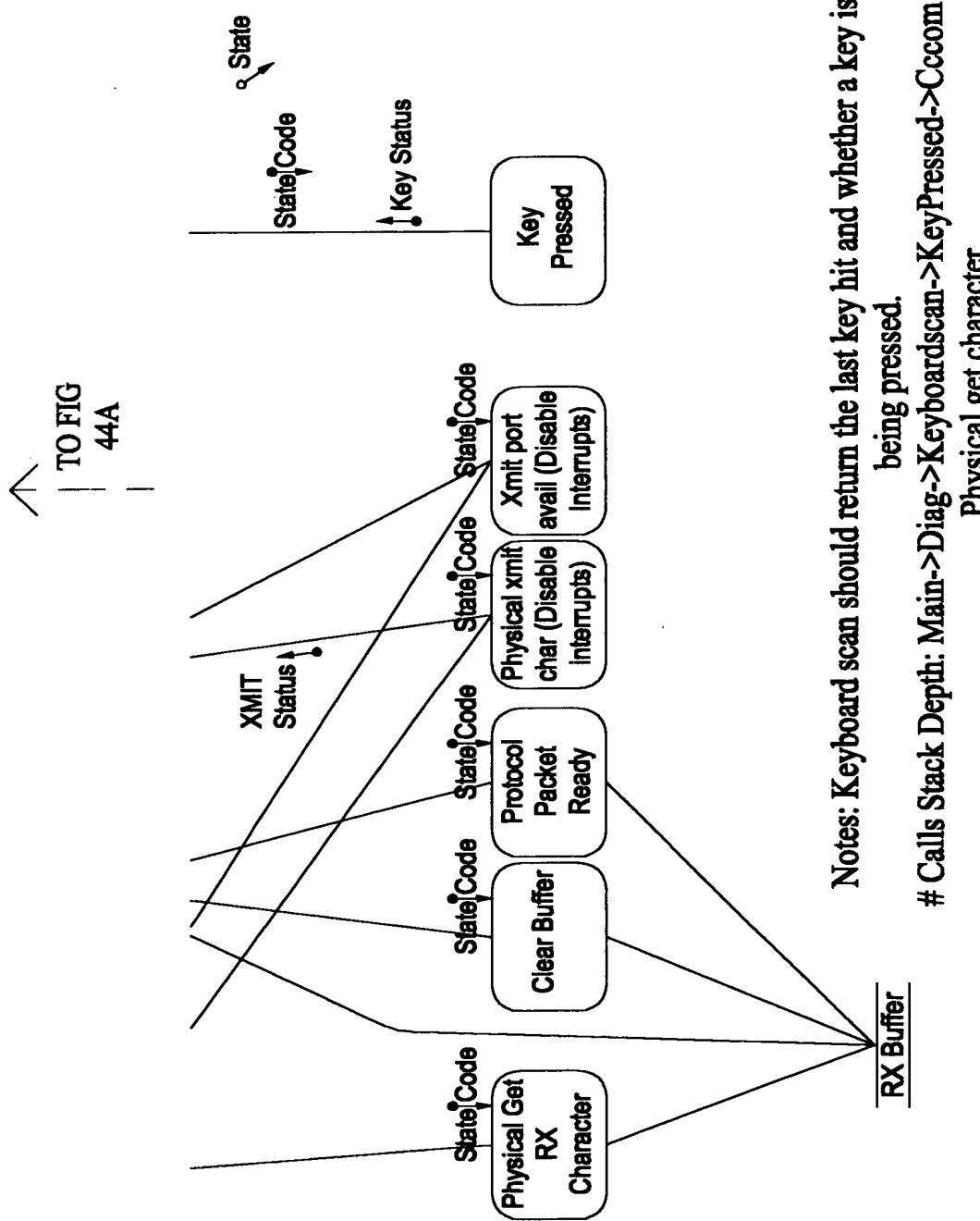


FIG. 44B



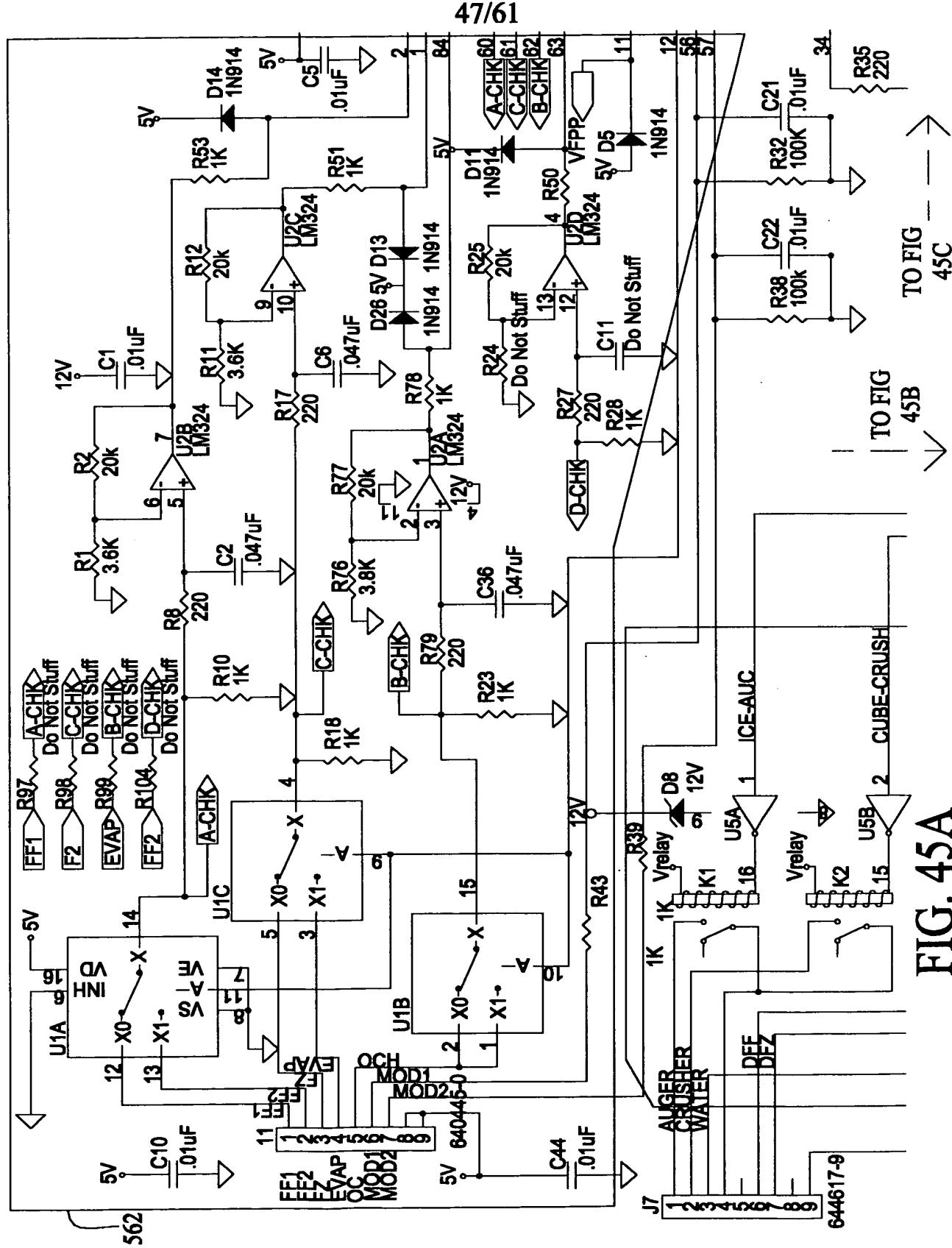
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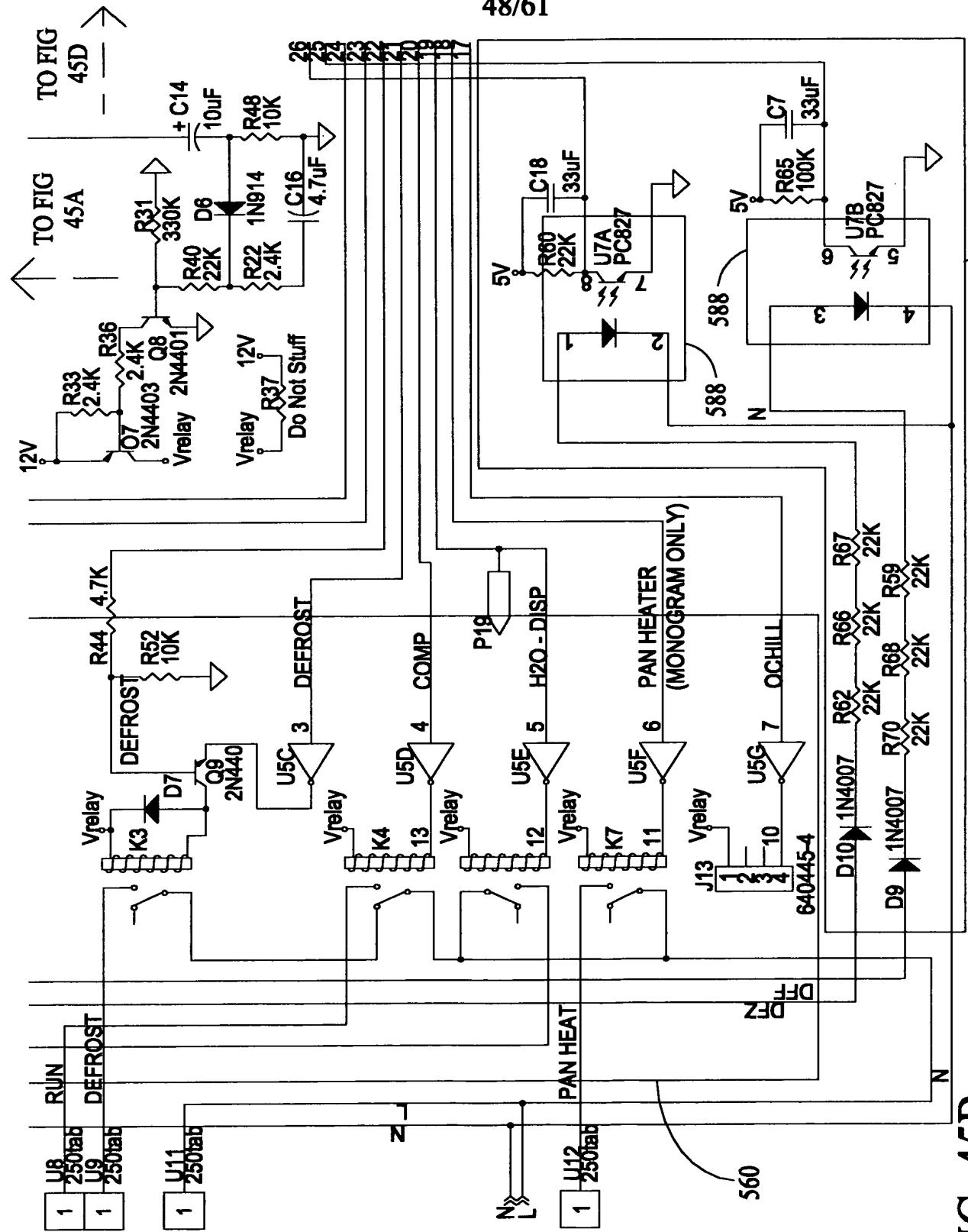


FIG. 45B

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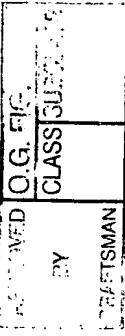
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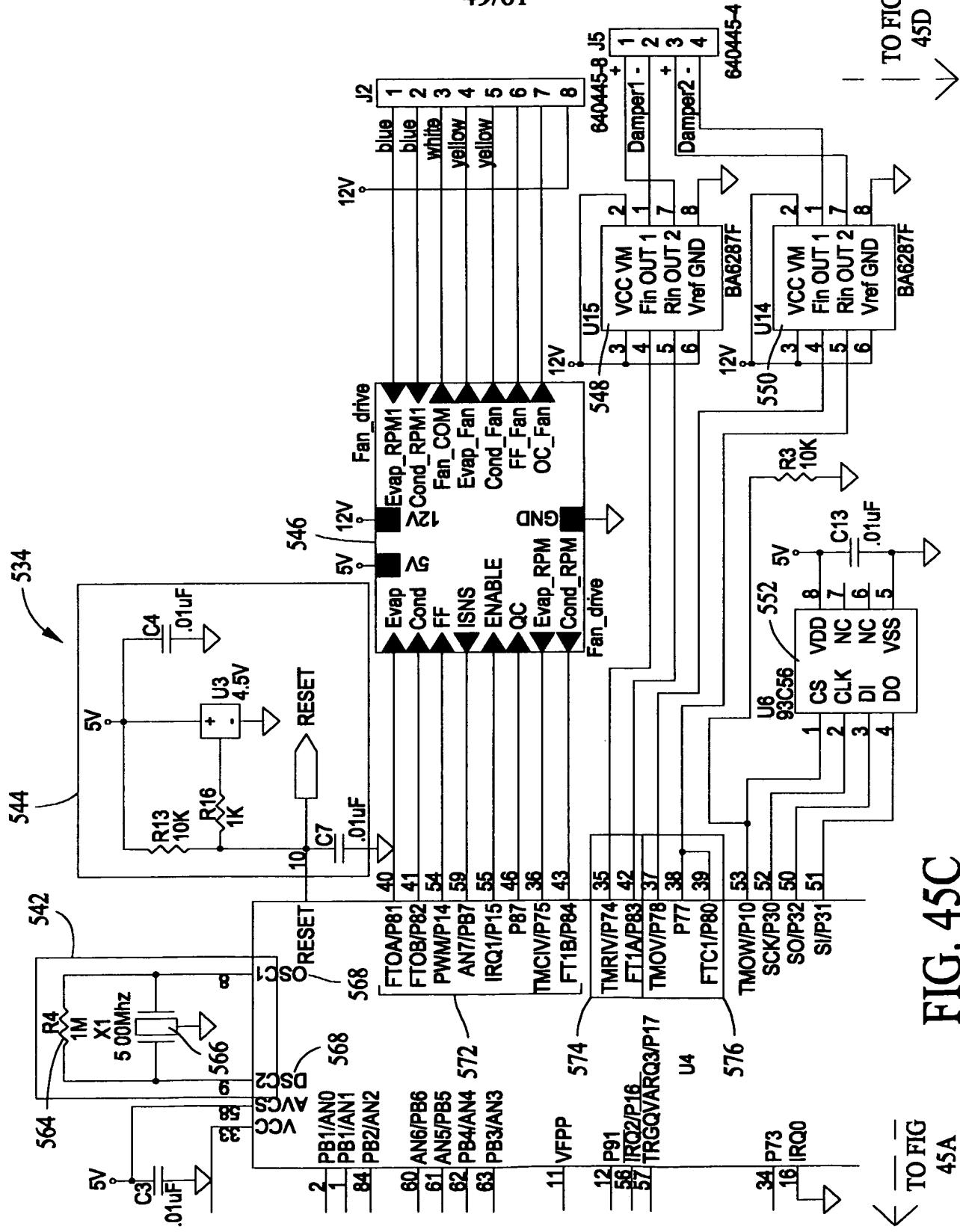


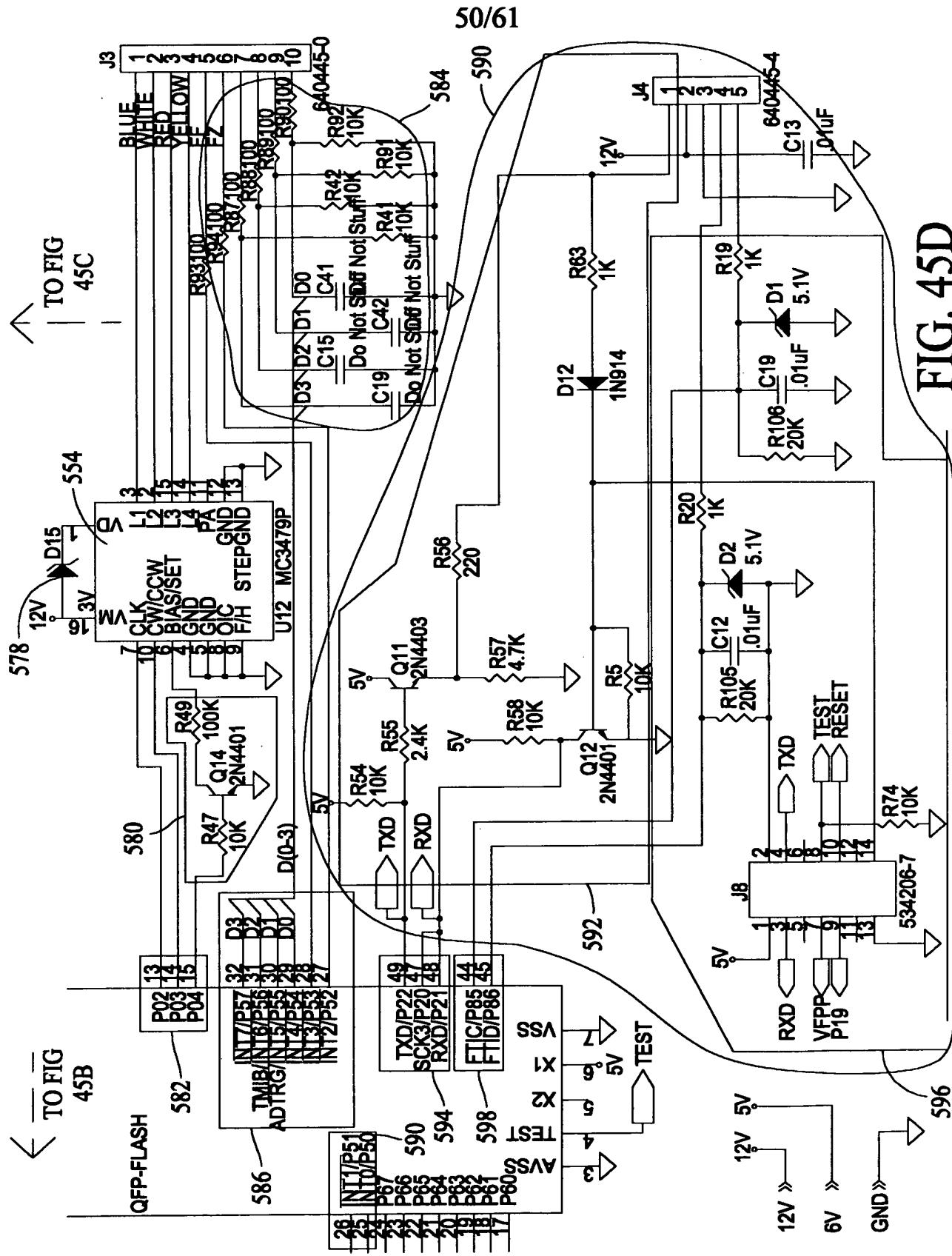
FIG. 45C

45A
TO FIG



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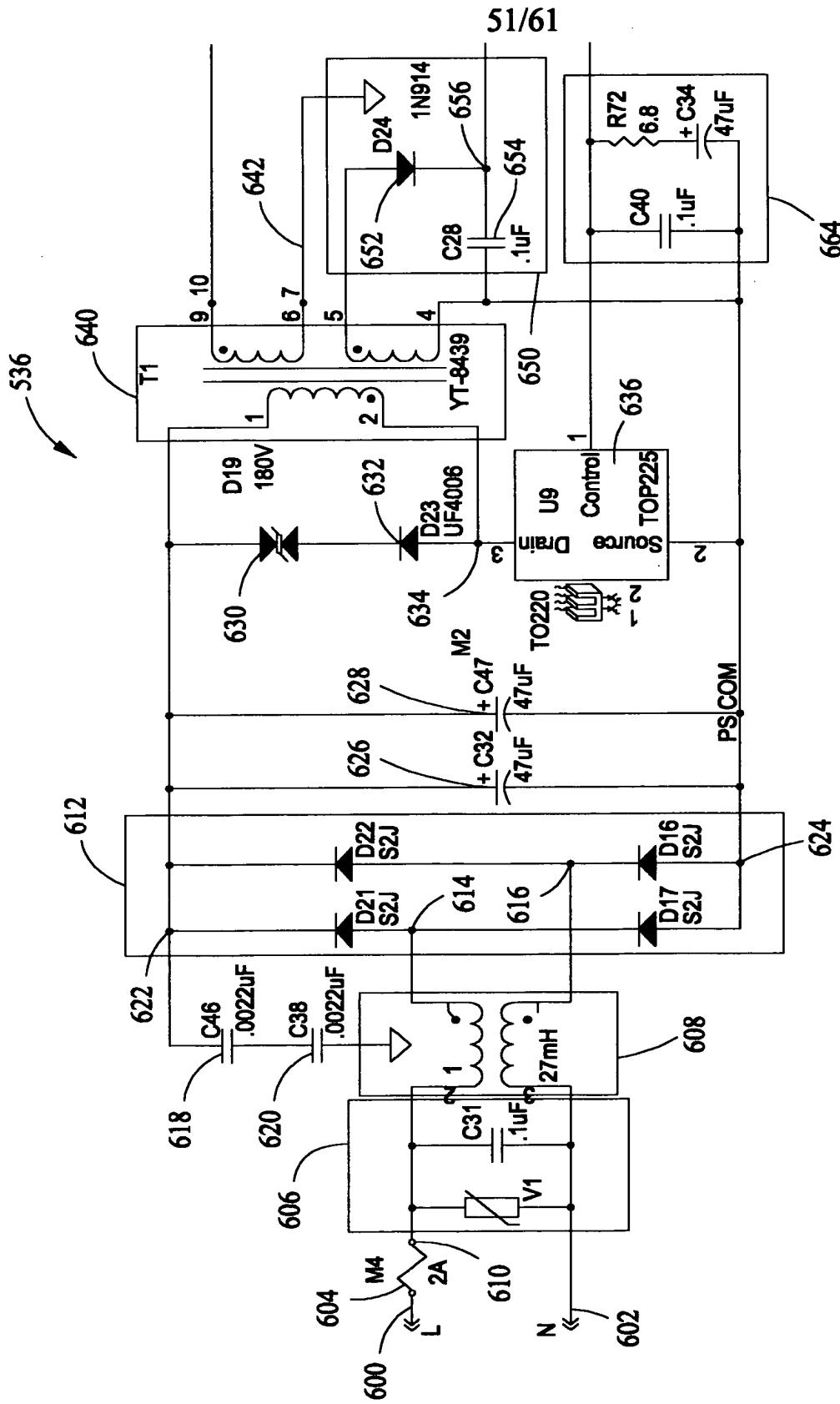


FIG. 45E



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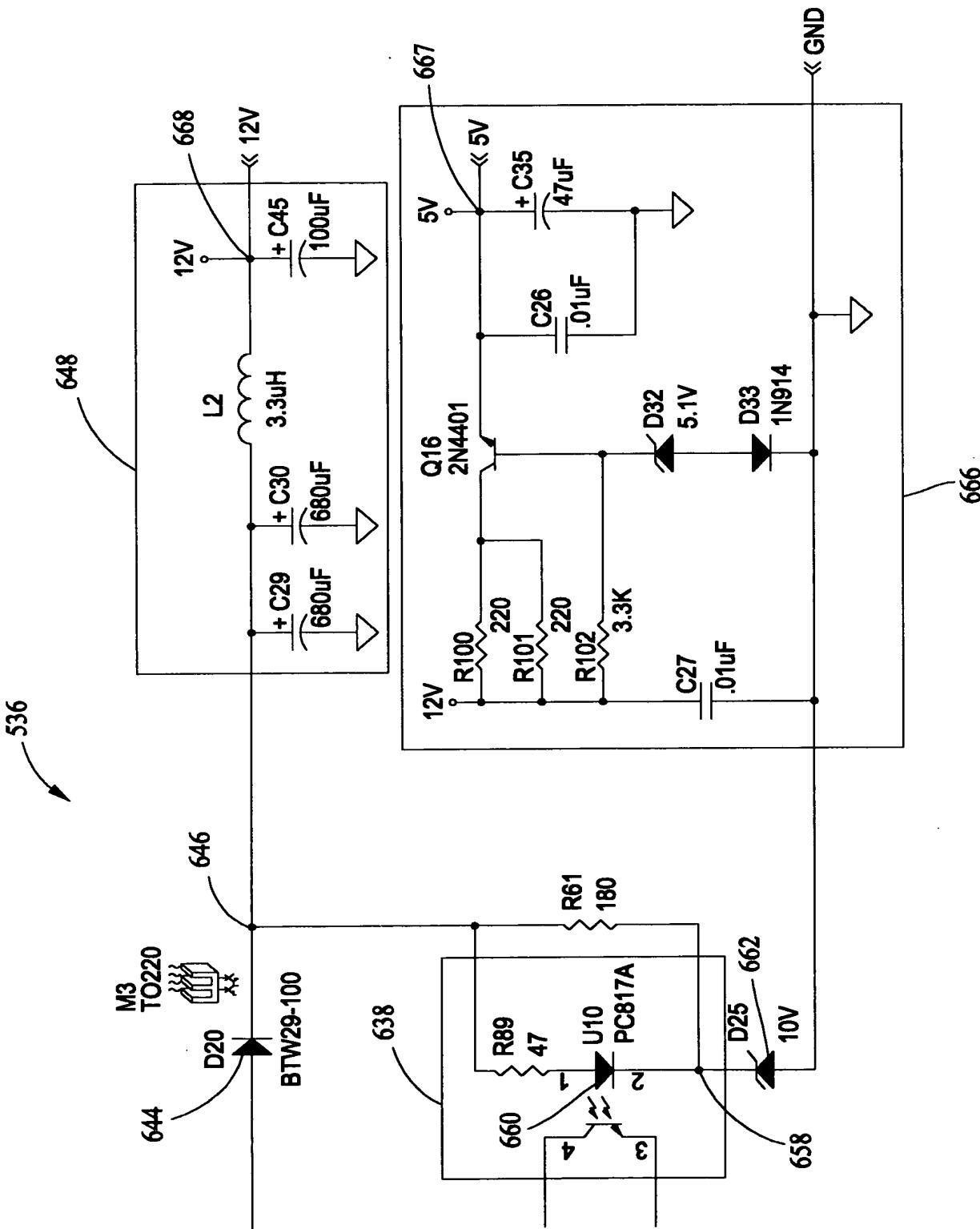
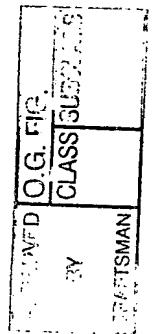
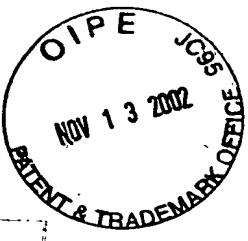
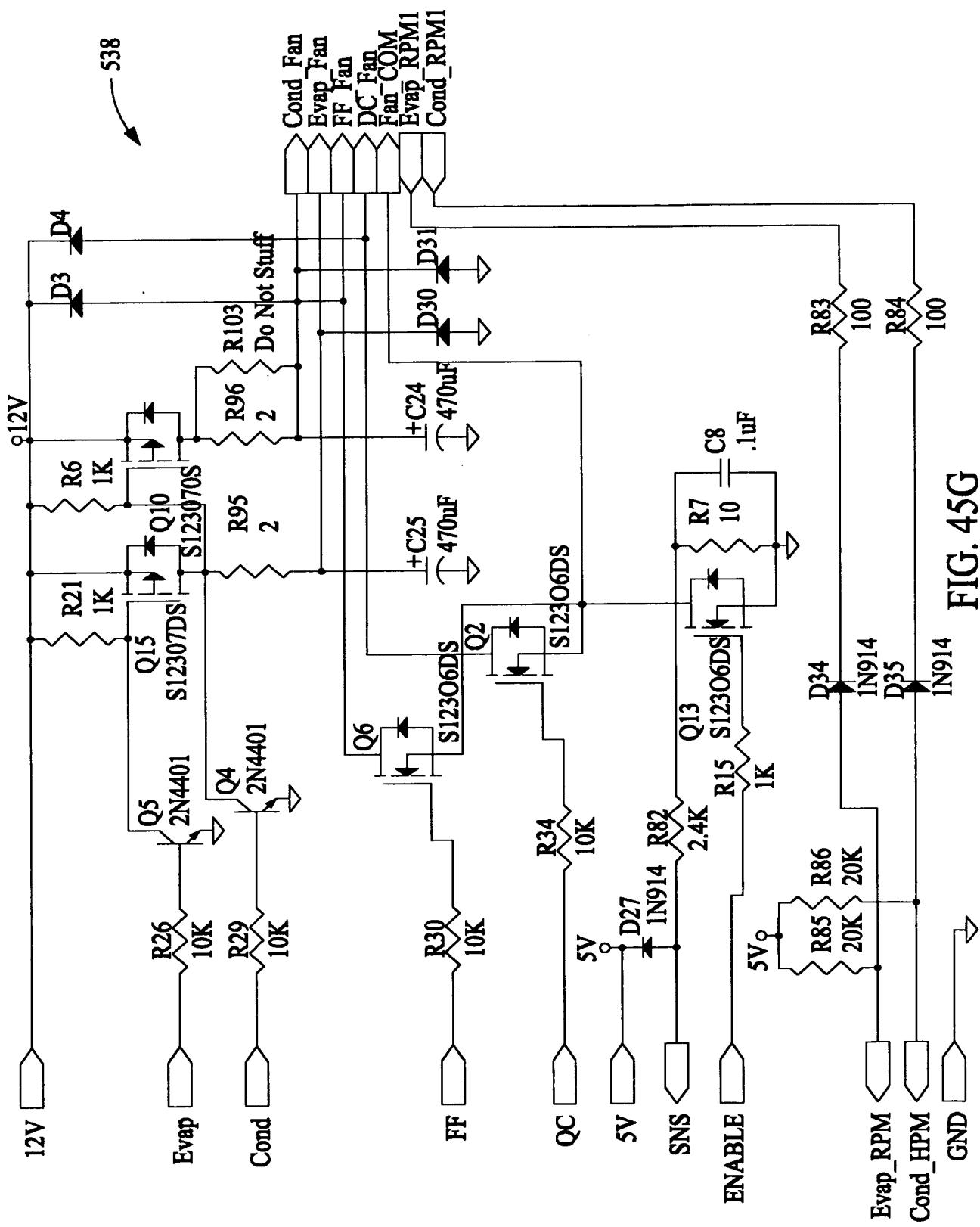


FIG. 45F



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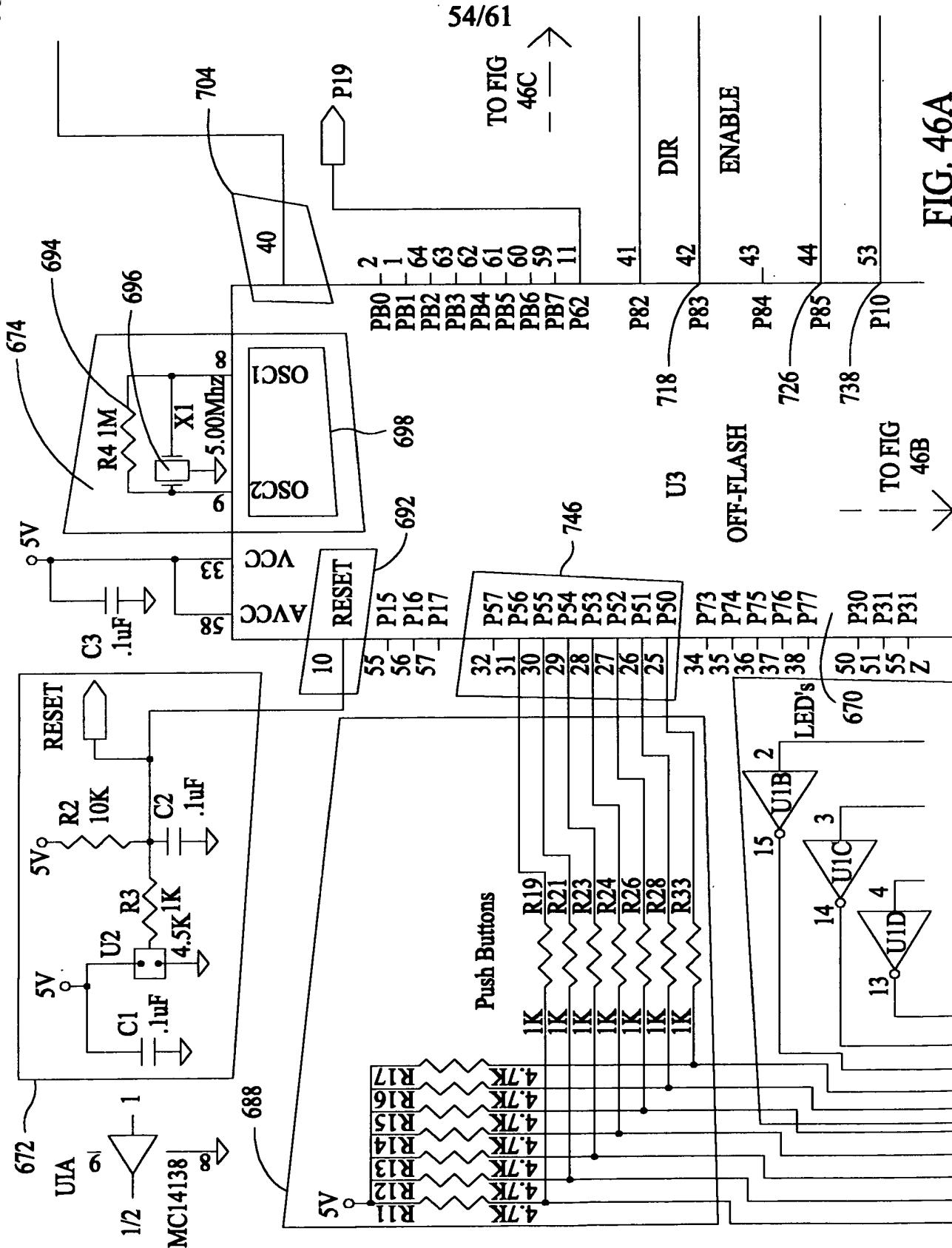
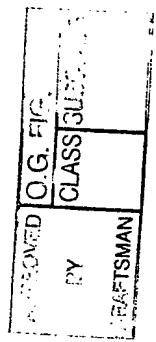


FIG. 46A

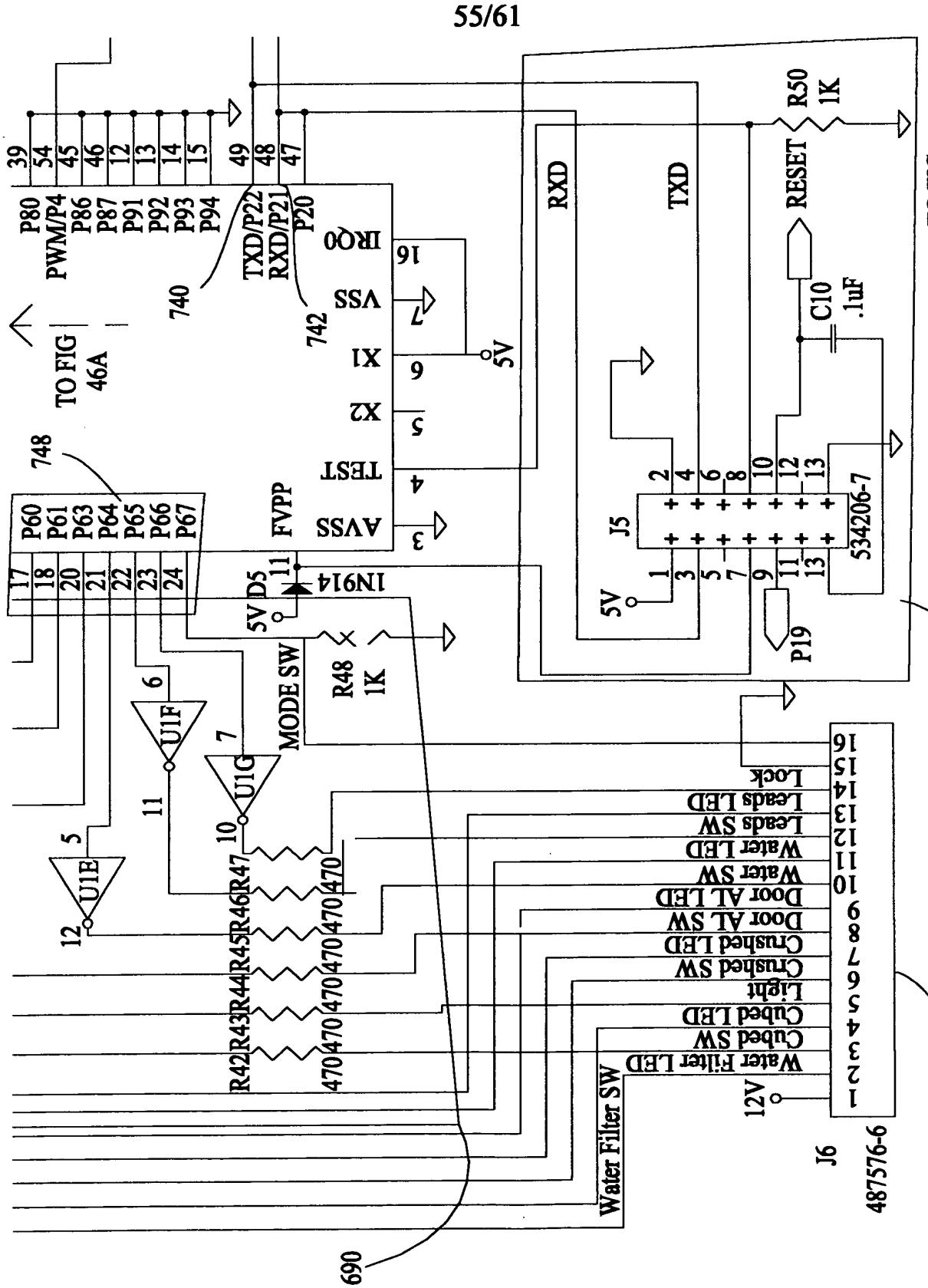


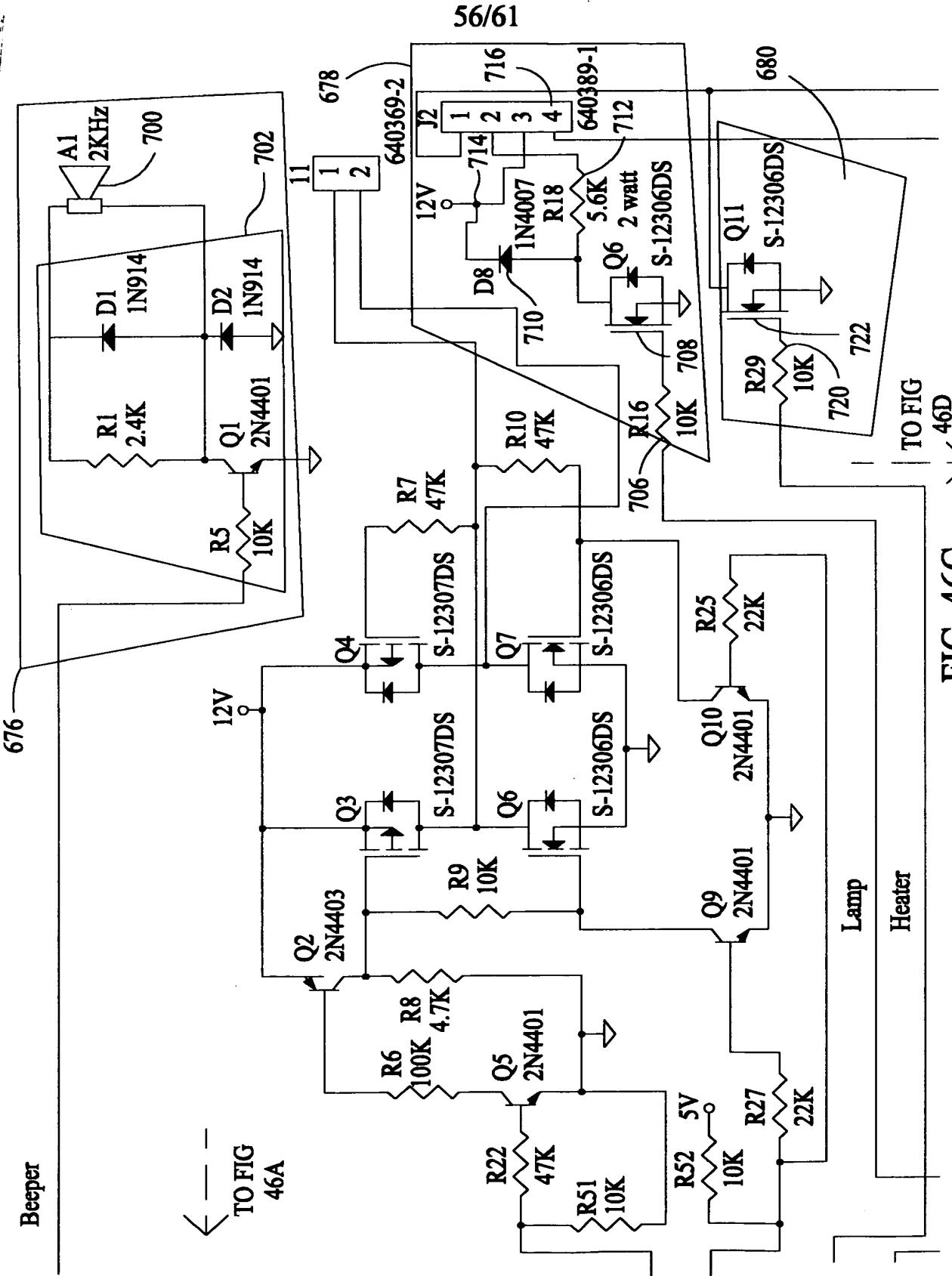
Applicant: Holmes et al.

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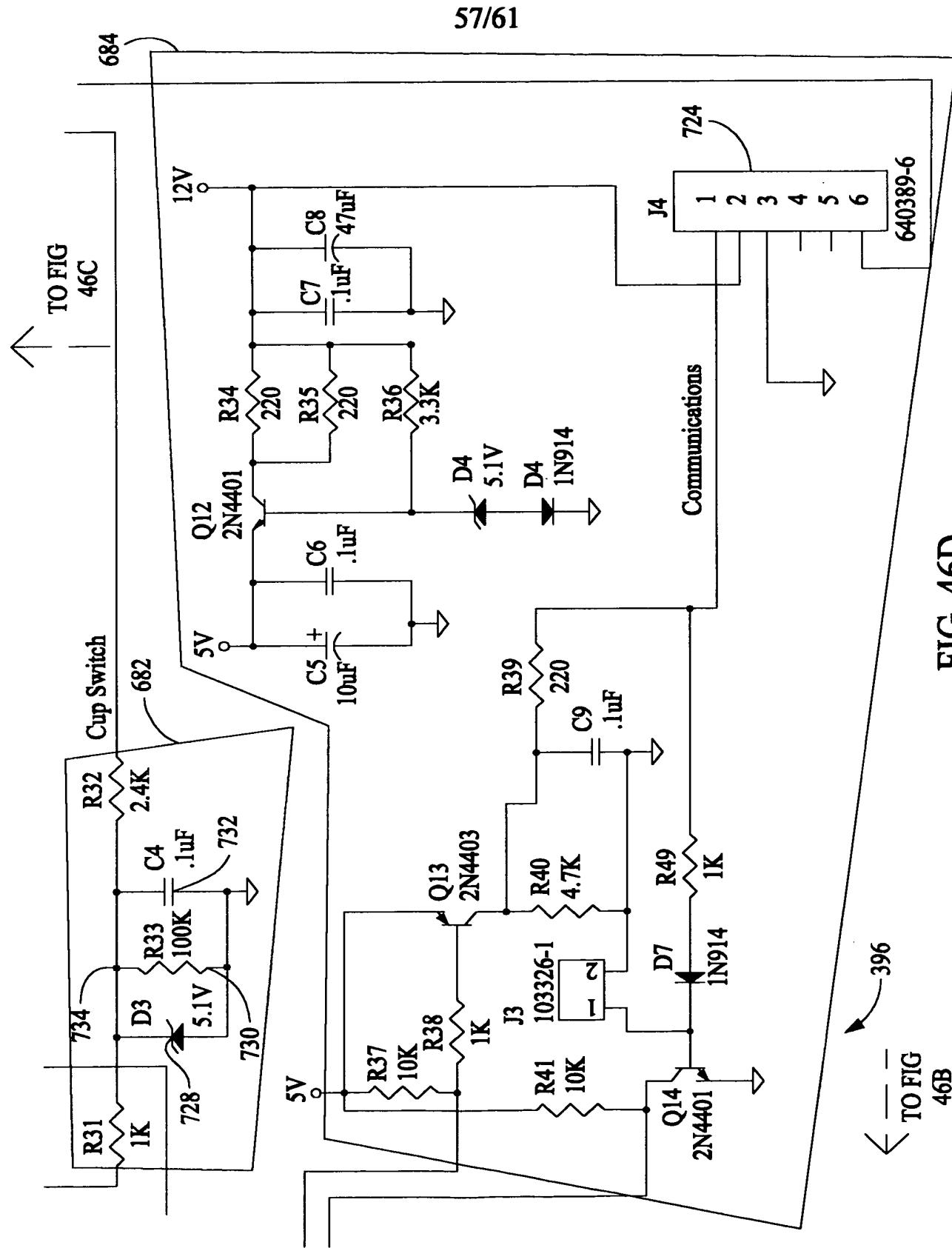
Title: REFRIGERATOR - ELECTRONICS ARCHITECTURE
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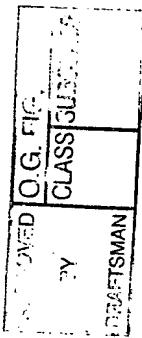




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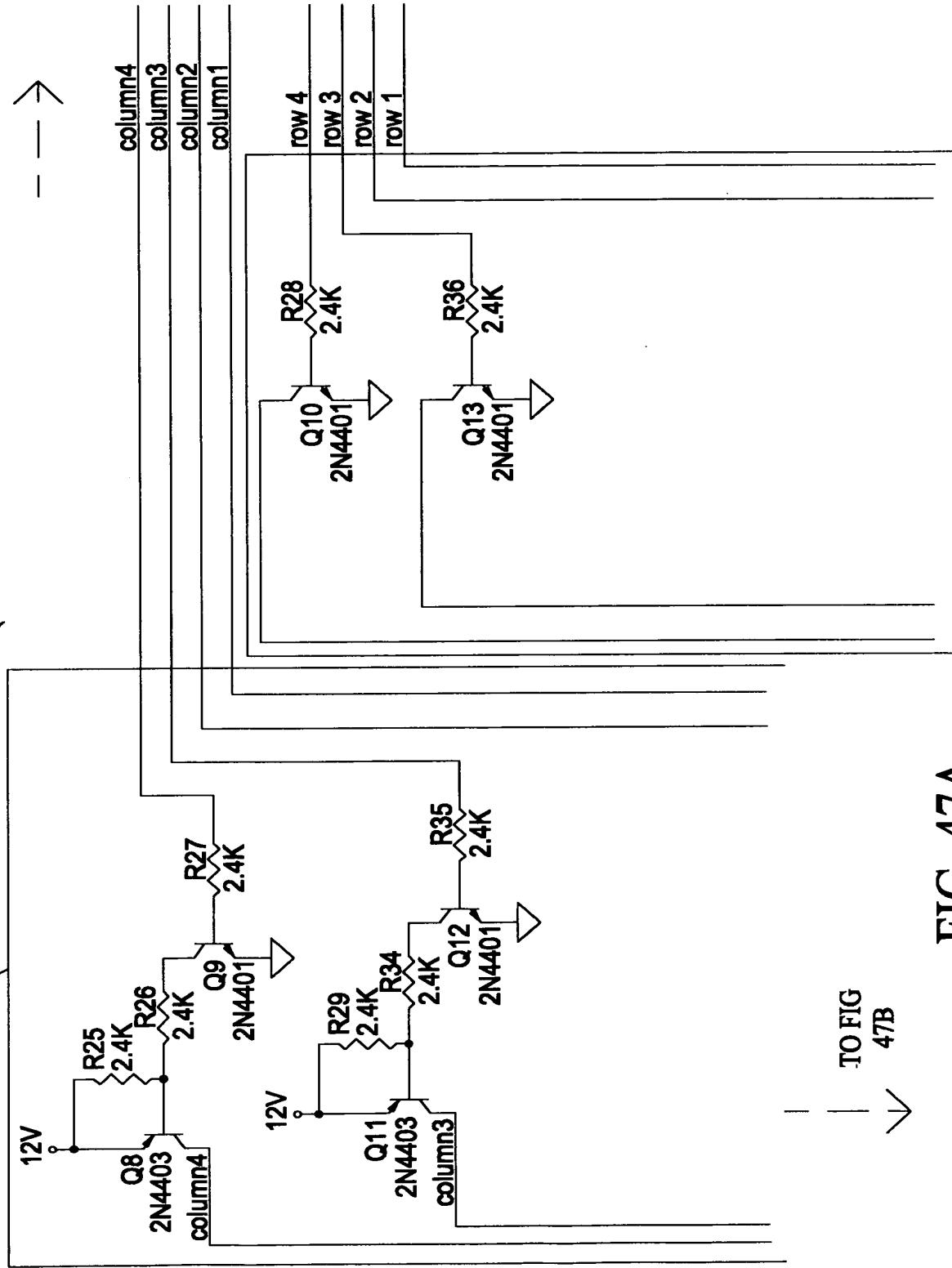


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TO FIG
47C

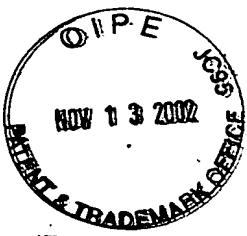


762 →
398



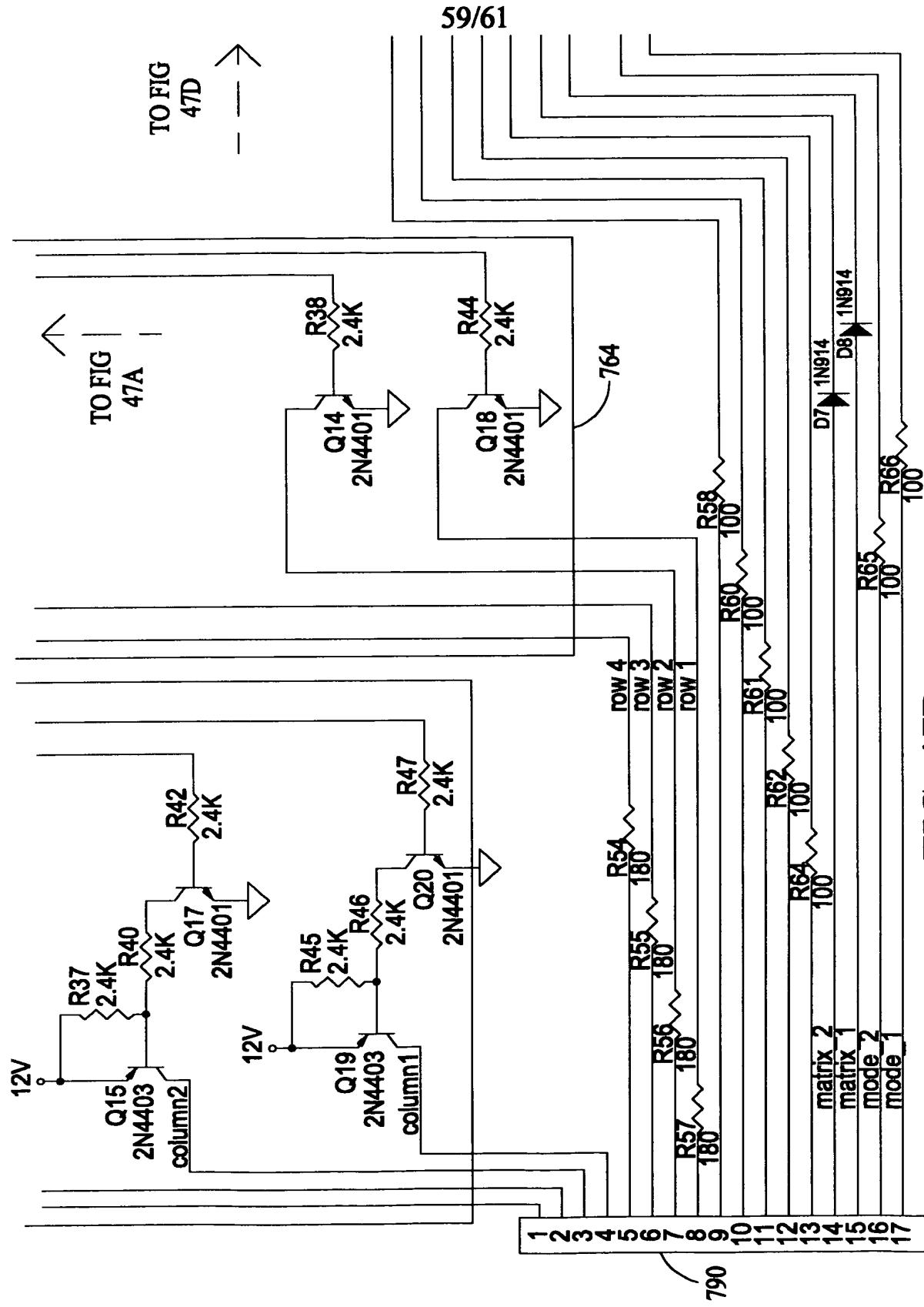
TO FIG
47B

FIG. 47A



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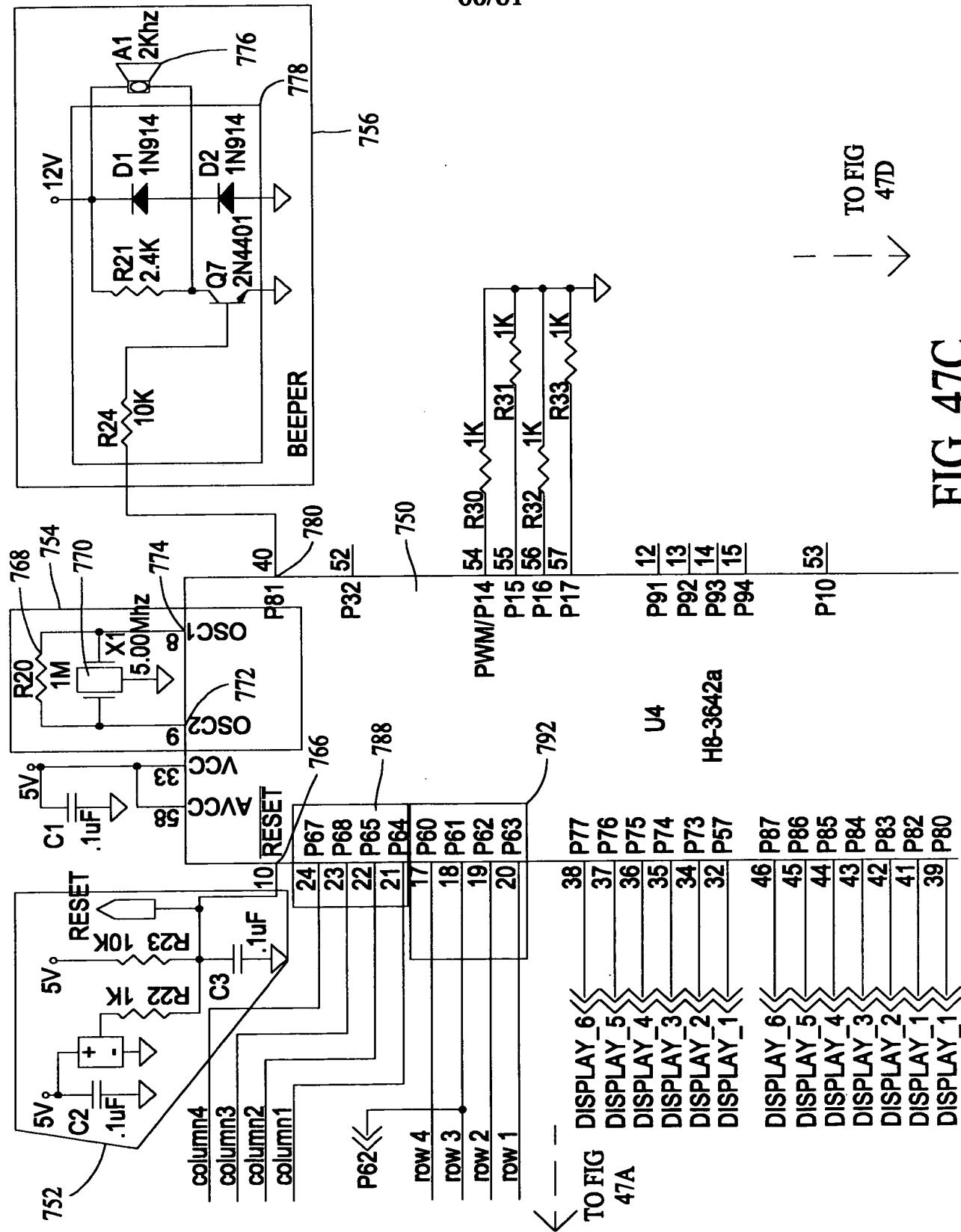


FIG. 47C



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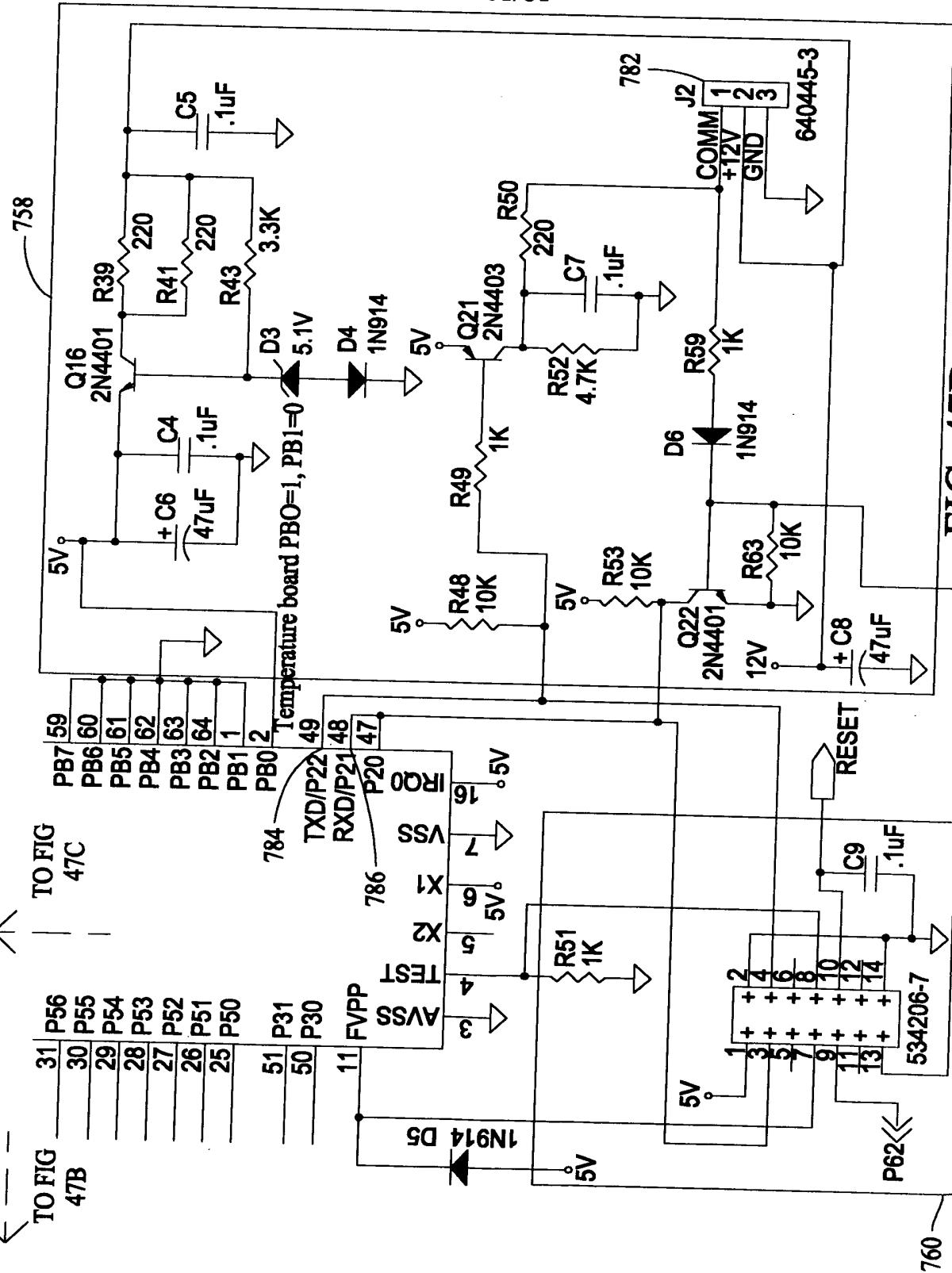


FIG. 47D